

A1

Connector

COMPONENT SIDE

ADGJ

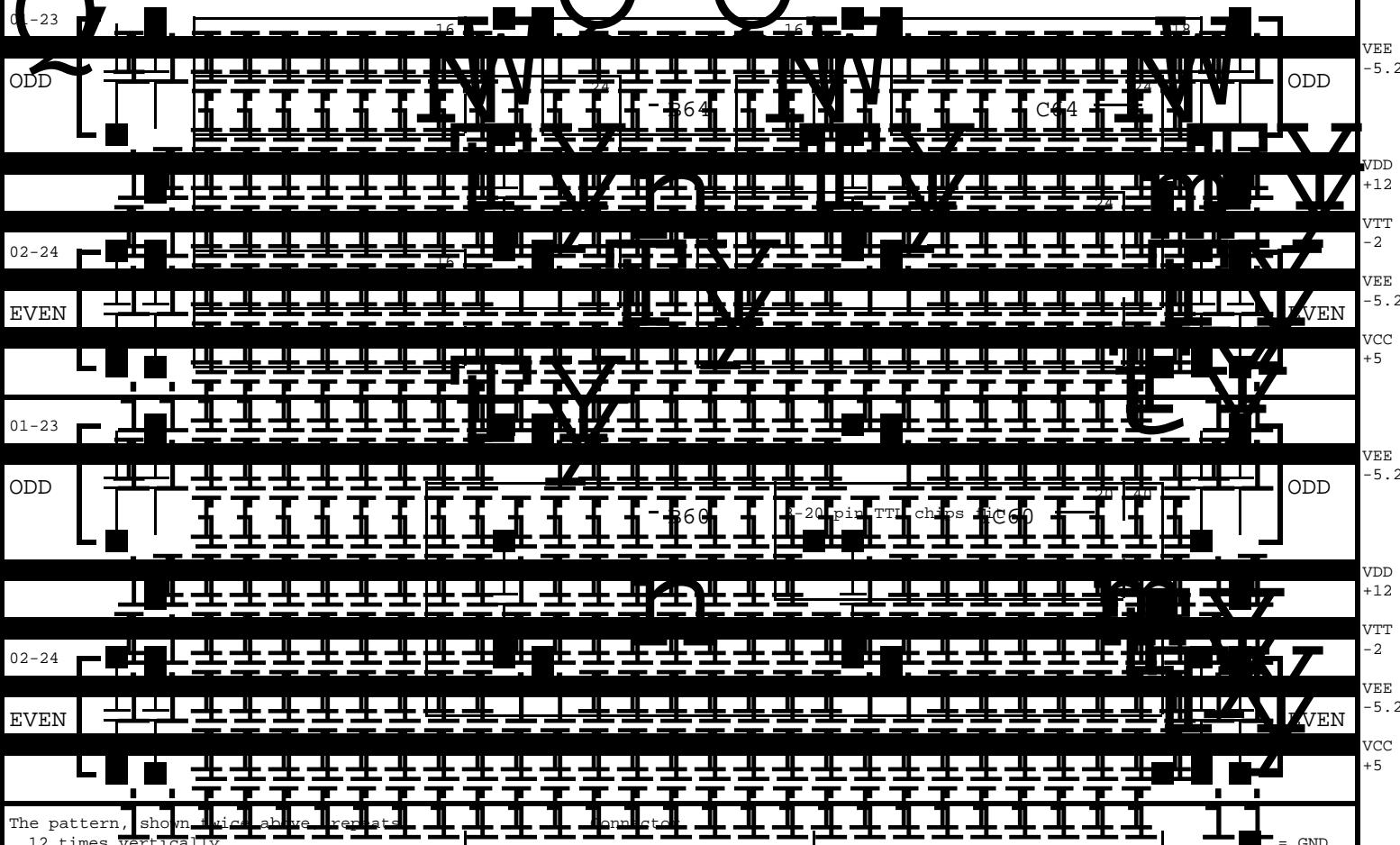
A1

BEHK

B1

CFIL

C1



The pattern, shown twice above, repeats  
12 times vertically  
4 times horizontally

ADGJ

BEHK

CFIL

## Notes:

1. The row numbers of the uncommitted rows, usable for ~~W1~~, etc. <sup>are</sup>: <sub>60</sub>
2. Originally, the uncommitted rows were numbered for 24-pin chips. <sup>65</sup>  
This is why pin 1 of the 24-pin chip in the B row is at B64, not <sup>66</sup> 5B64!
3. 0.4" wide chips can be inserted by bending the legs out to 0.45". <sup>67</sup>  
<sup>68</sup>
4. The syntax for specifying the board location of pin 1 of a chip is: <sup>69</sup>

# { - } R-offset { { ^ } { - } D-offset } BoardLocation Modifier

where R-offset is the number of positions DOWN in the picture above.  
D-offset is the number of positions LEFT in the picture above.  
{ and } are metasymbols denoting optional omission.  
Modifier may be ! or %, etc.

Example: #3^1c1 is the location of the 24-pin 0.4" dip in the picture above.  
Example: #-1^-5B64 is the same as B1  
(\_ and ^ actually specify offsets Right and Down relative to the chip vertically)

