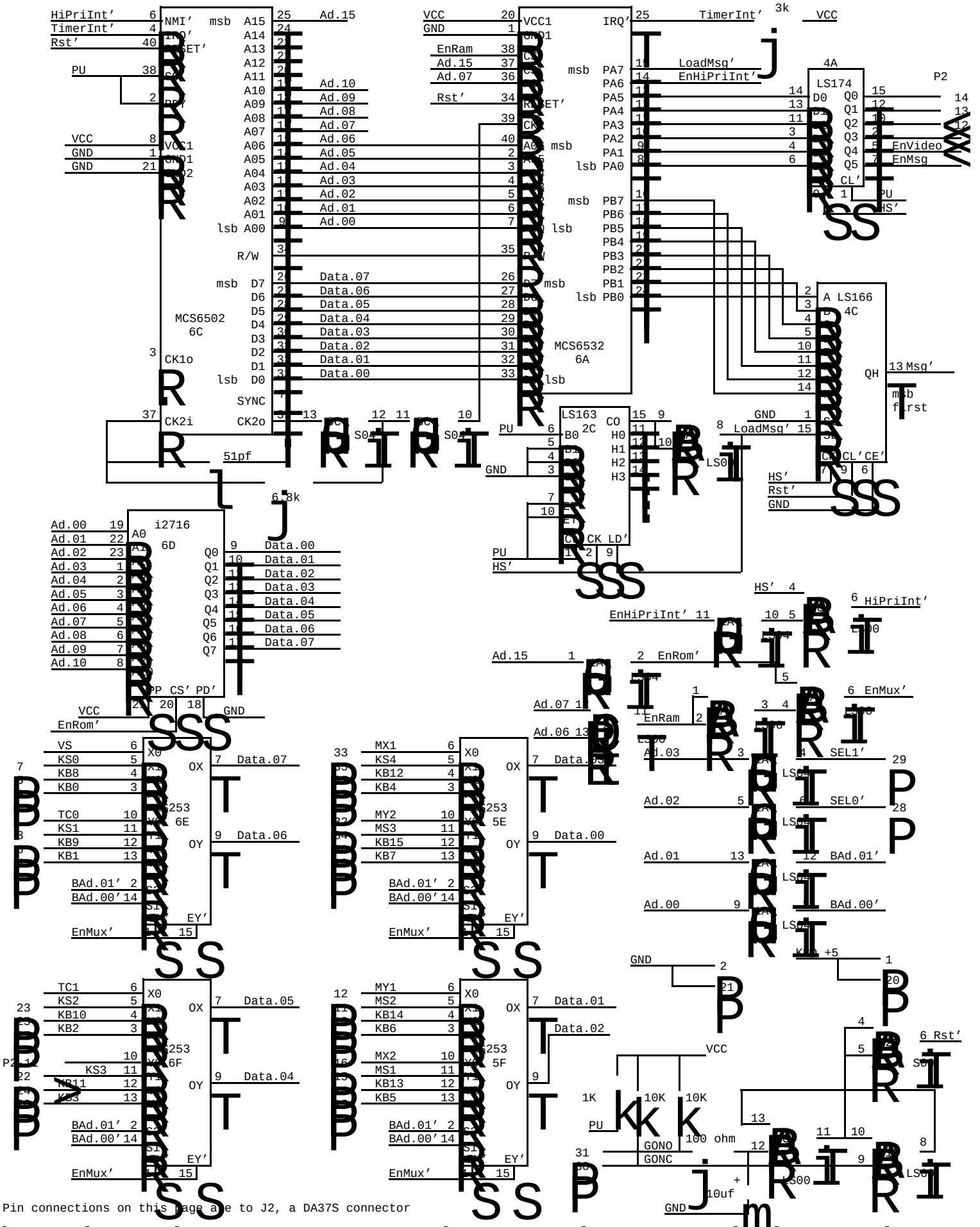
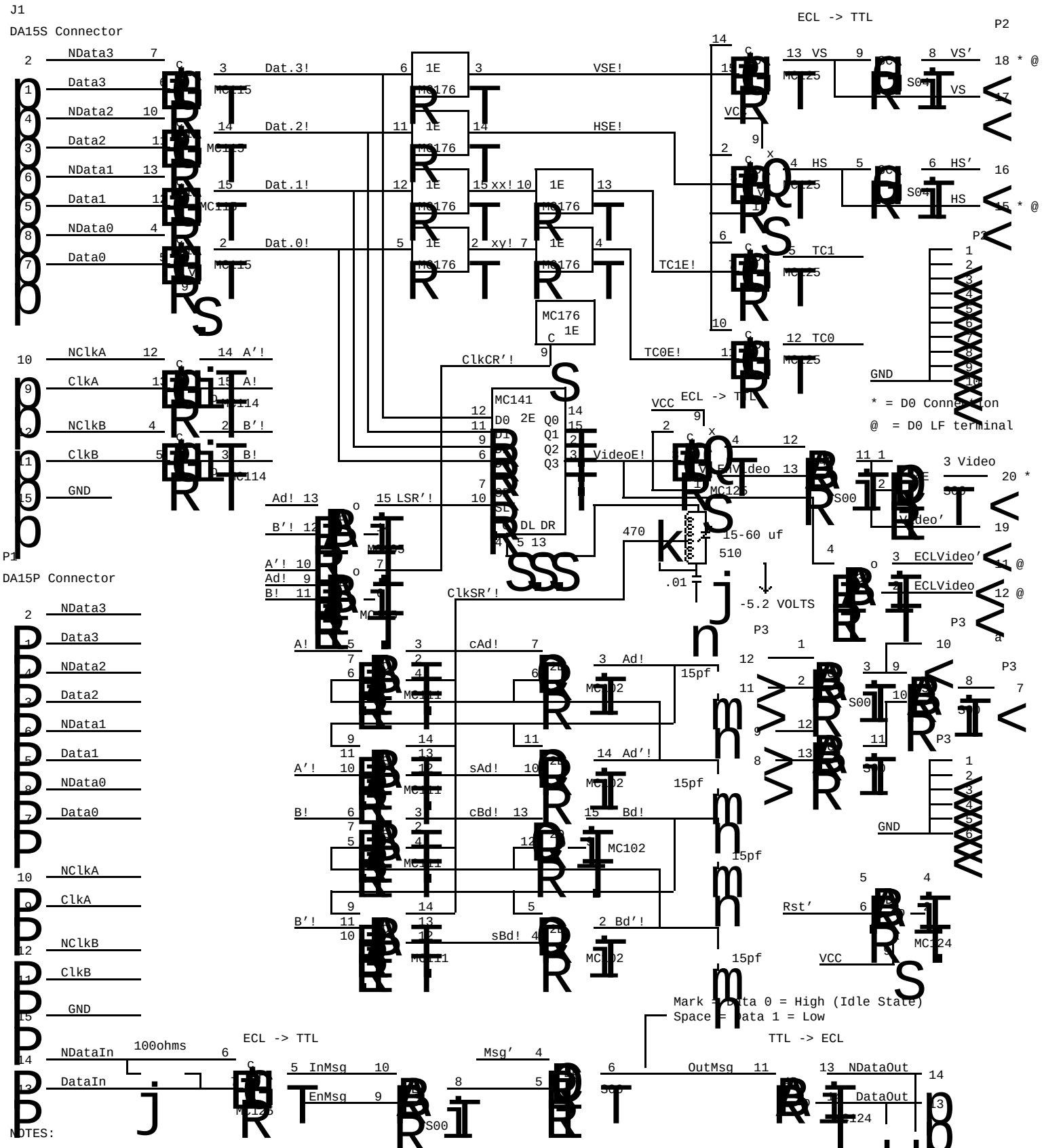


XEROX PARC	Project D0	7-Wire Terminal Interface Backchannel	File Terminal01.si	Designer Thacker	Rev Gb	Date 12/26/79	Page 1

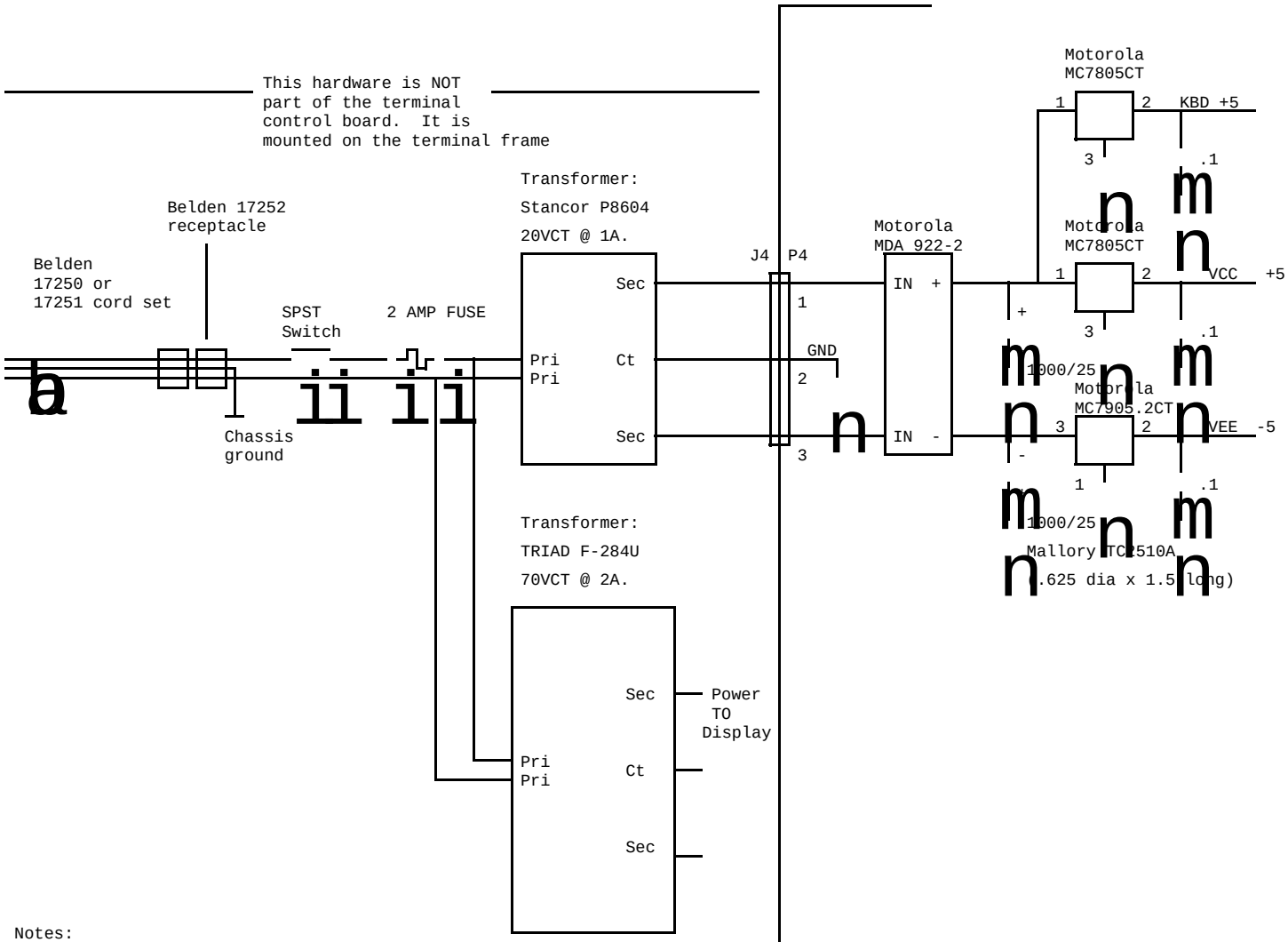


NOTES:

1) All signals with names of the form xxx! have 510ohm pulldown resistors to VEE (-5.2) (26 places)
2) P2 consists of wire-wrap posts on .100 centers
3) Ground, VCC, and VEE pins for ECL chips are as shown:

	GND	VEE	VCC
MC102, MC105, MC114, MC115 MC141	1,16	8	-
MC111	1,15,16	8	-
MC124, MC125	16	8	9
MC173, 176	16	8	-

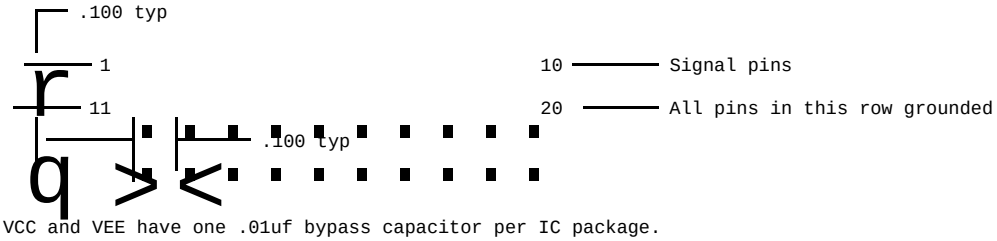
XEROX PARC	Project D0	7-Wire Terminal Interface Video Interface	File Terminal02.si	Designer Thacker	Rev Gb	Date 12/26/79	Page 2



Notes:

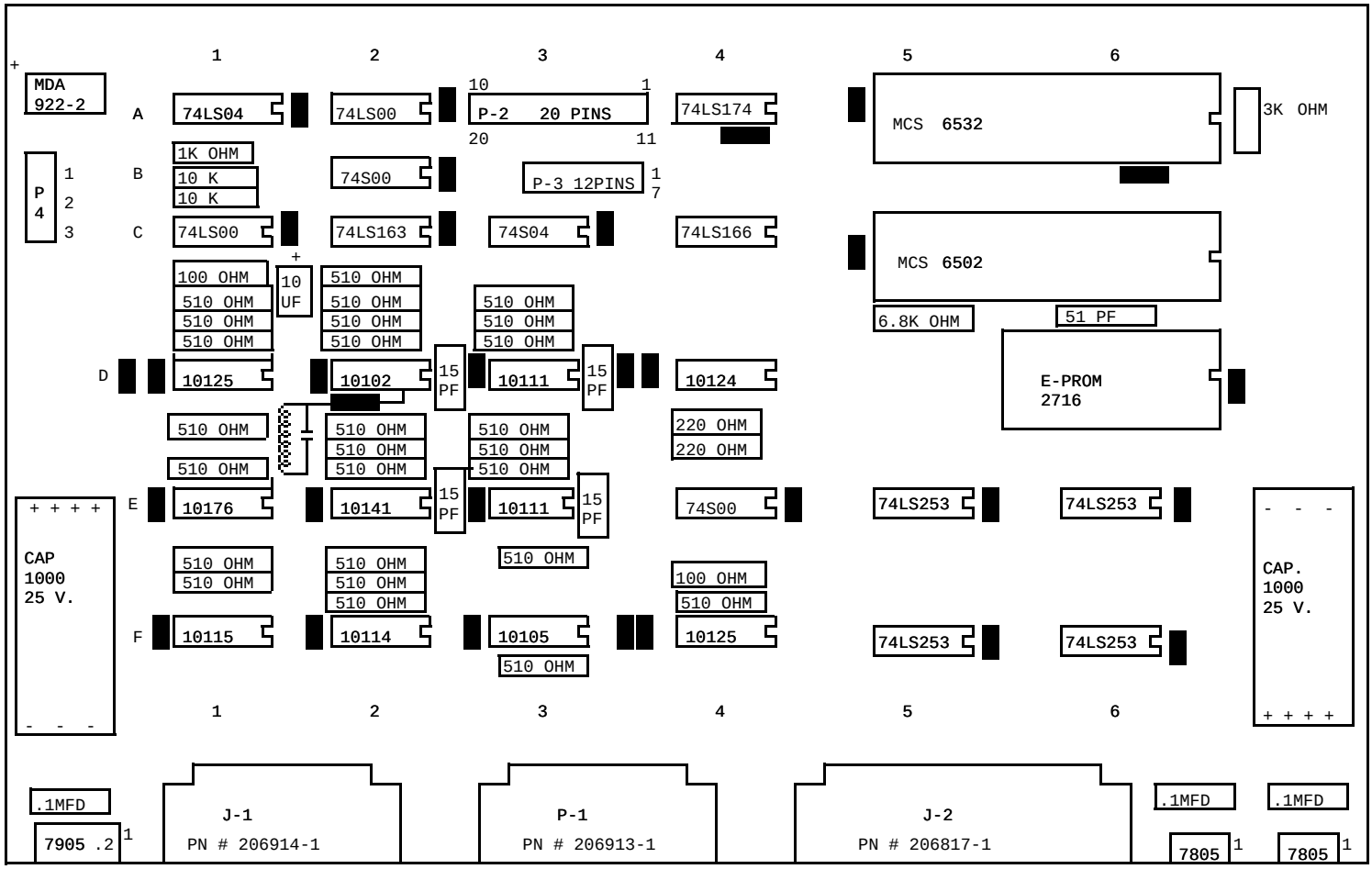
1) J4 is Waldom/Molex 03-09-2031 housing
with 02-09-1118 FEMALE terminals.
P4 consists of three Molex 02-09-2134 pins
mounted on the PCB on .200 centers. The hole
diameter for these pins is .093

2) P2 consists of an array of .025 square wire wrap posts.
The finished hole ID for these posts should be .035.
There should be two rows of pins. Only one row is shown
on the logic diagrams; all pins in the other row are connected
to ground:



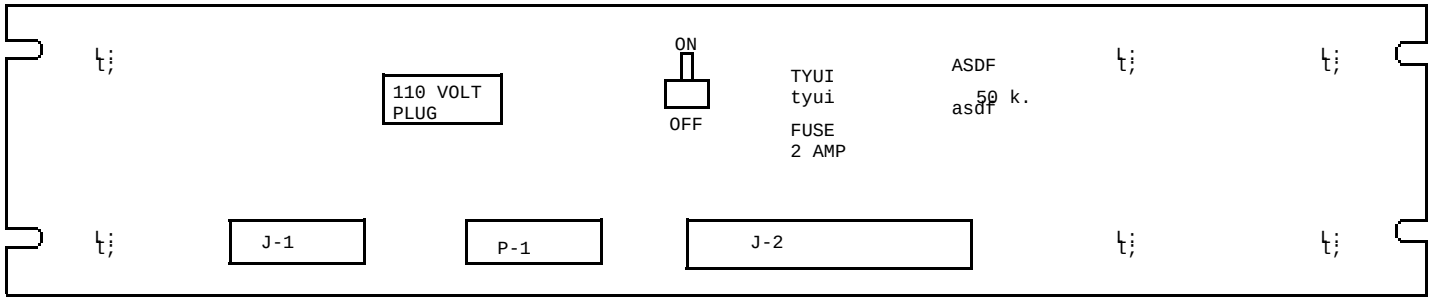
3) VCC and VEE have one .01uf bypass capacitor per IC package.

XEROX PARC	Project D0	7-Wire Terminal Interface Power Supply	File Terminal03.si	Designer Thacker	Rev GA	Date 4/14/79	Page 3

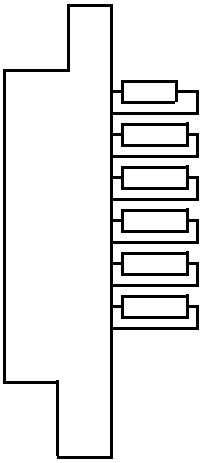


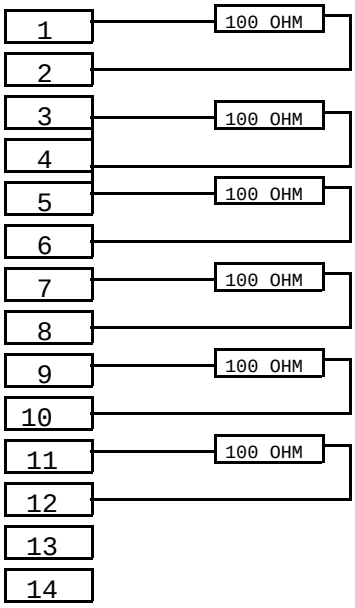
NOTES
1. ■ IS A .01 UF CAP.
2. ■ IS A .01 UF CAP.

BACKPANEL



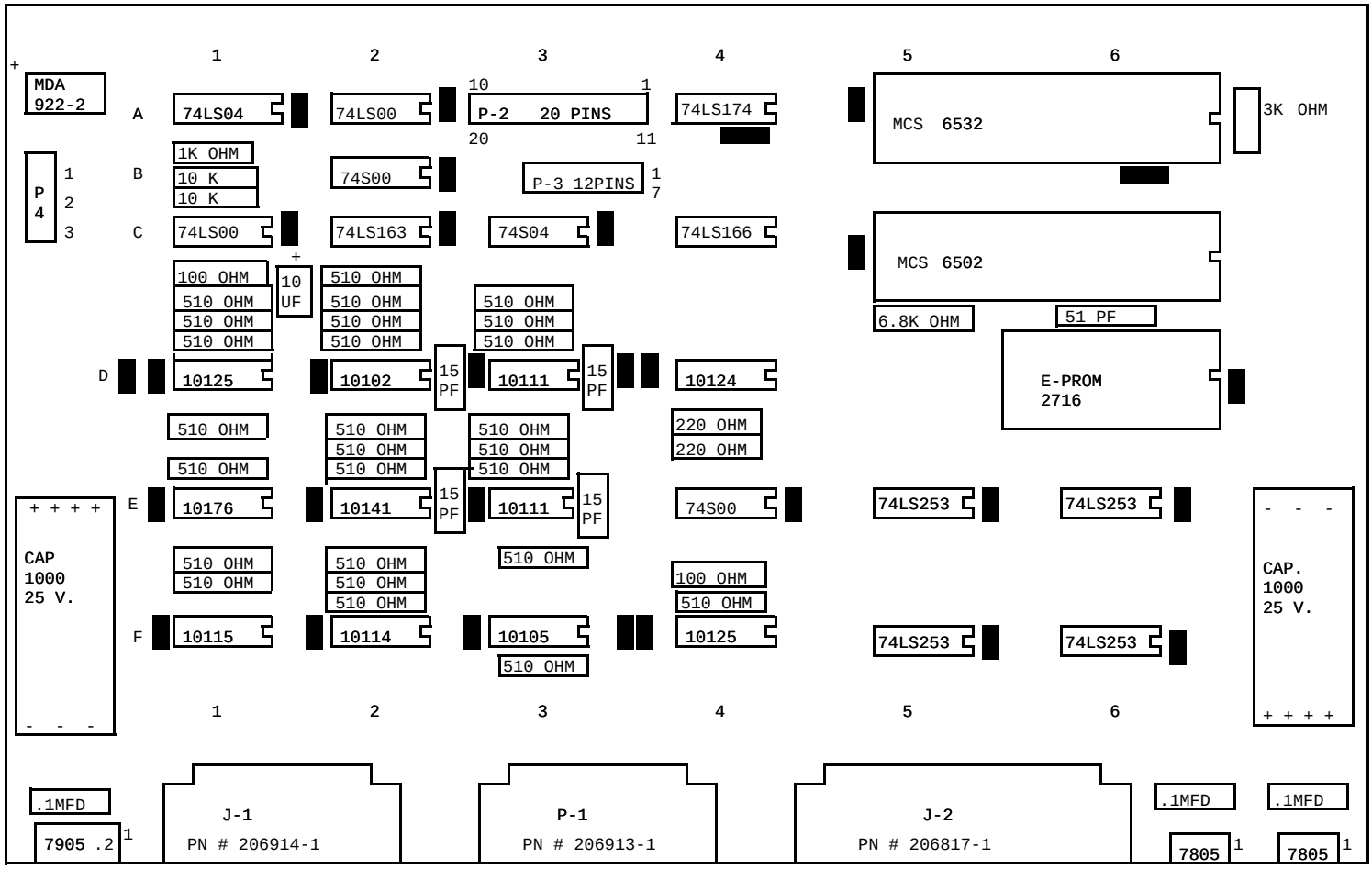
XEROX GARAGE	Project D0	7-WIRE TERMINAL INTERFACE	IVY<VEST> File TERMINALLAYOUT-GC.SIL	Designer VEST	Rev GB	Date 5-16-81	Page 01





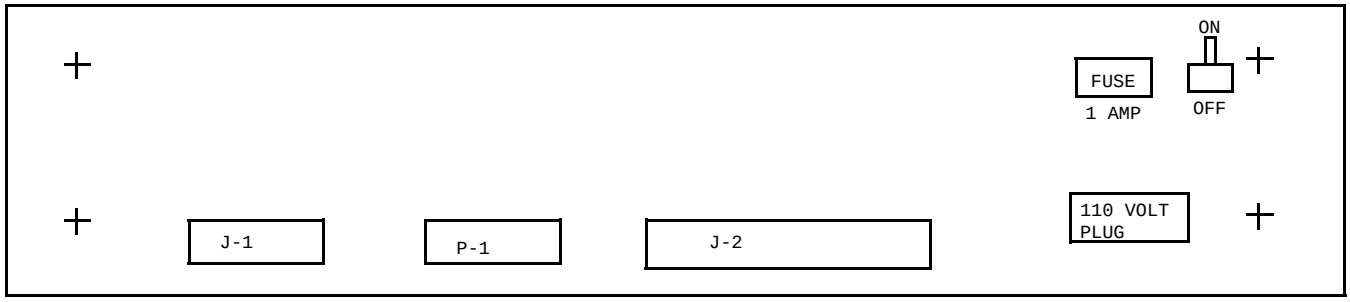
NOTES
RESISTORS ARE 100 OHM 1/4 WATT
TERMINATOR CONNECTOR IS DA 15S

XEROX PARC	Project D0	TERMINAL CABLES	File IBIS <VEST> TERMINATOR.SIL	Designer VEST	Rev B	Date 10-15-80	Page 1

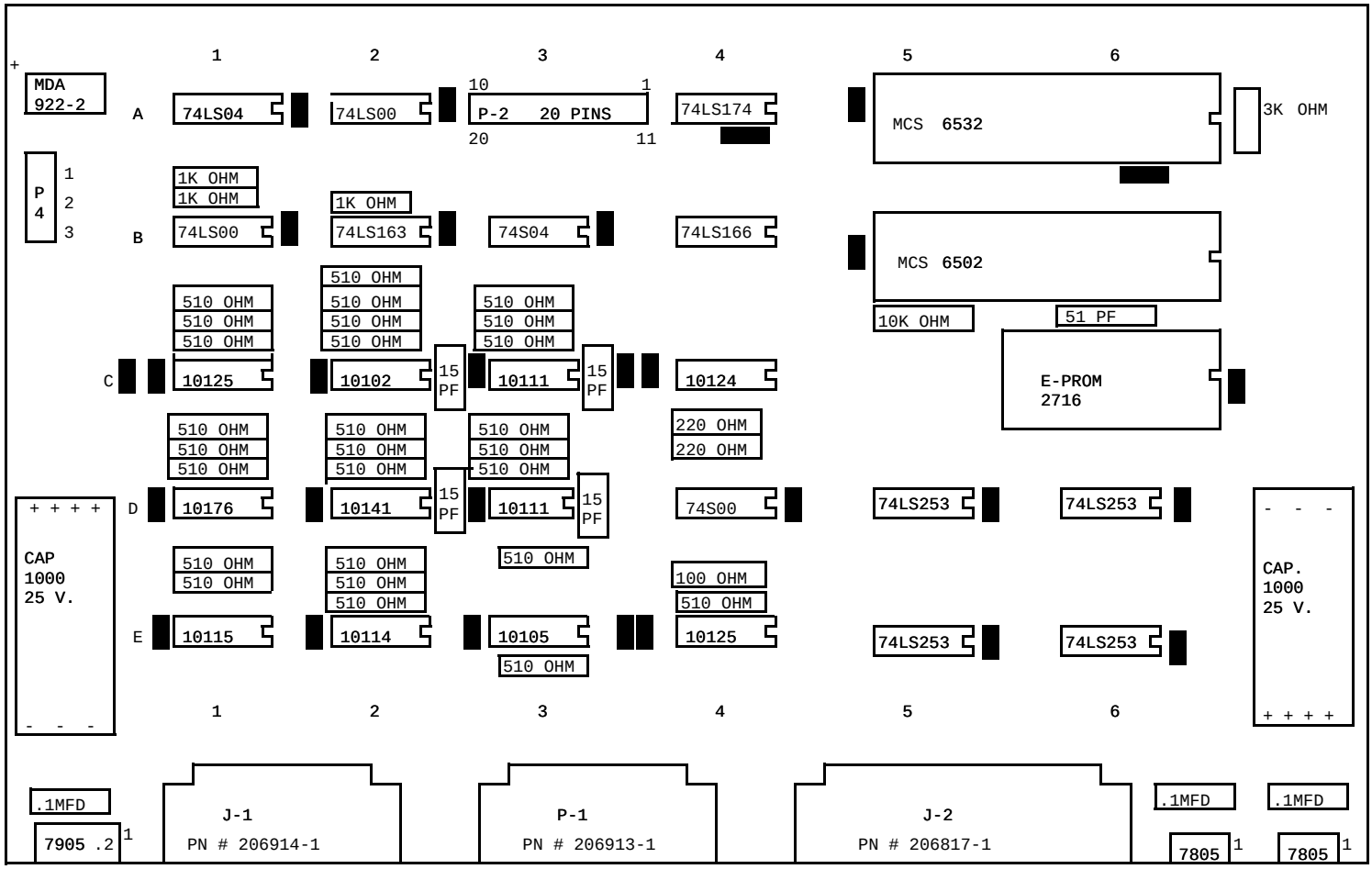


NOTES
1. ■ IS A .01 UF CAP.
2. ▮ IS A .01 UF CAP.

BACKPANEL



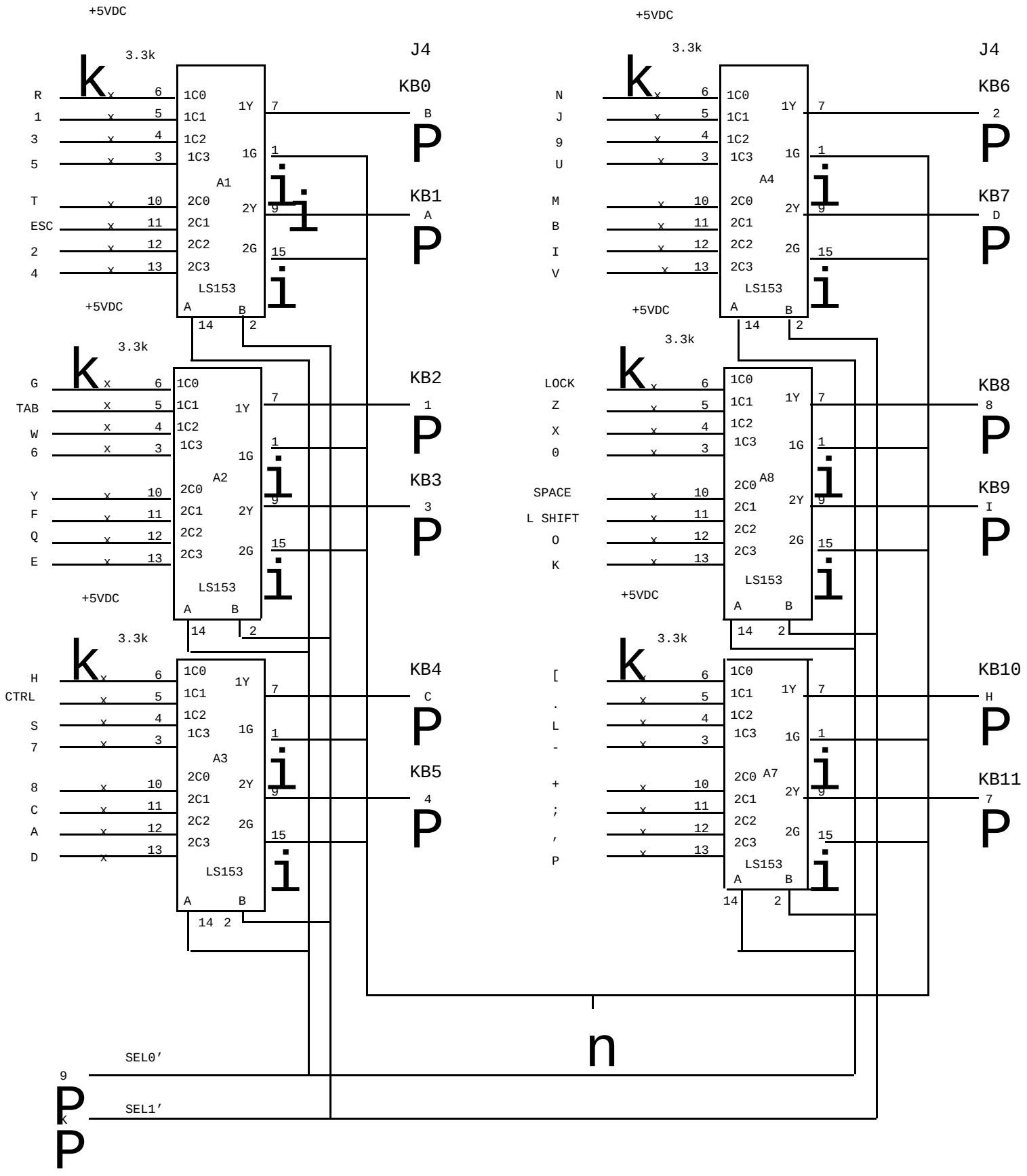
XEROX GARAGE	Project D0	7-WIRE TERMINAL INTERFACE	File IVY<VEST> 7WIRELAYOUT-B.SIL	Designer VEST	Rev GB	Date 11-2-79	Page 01



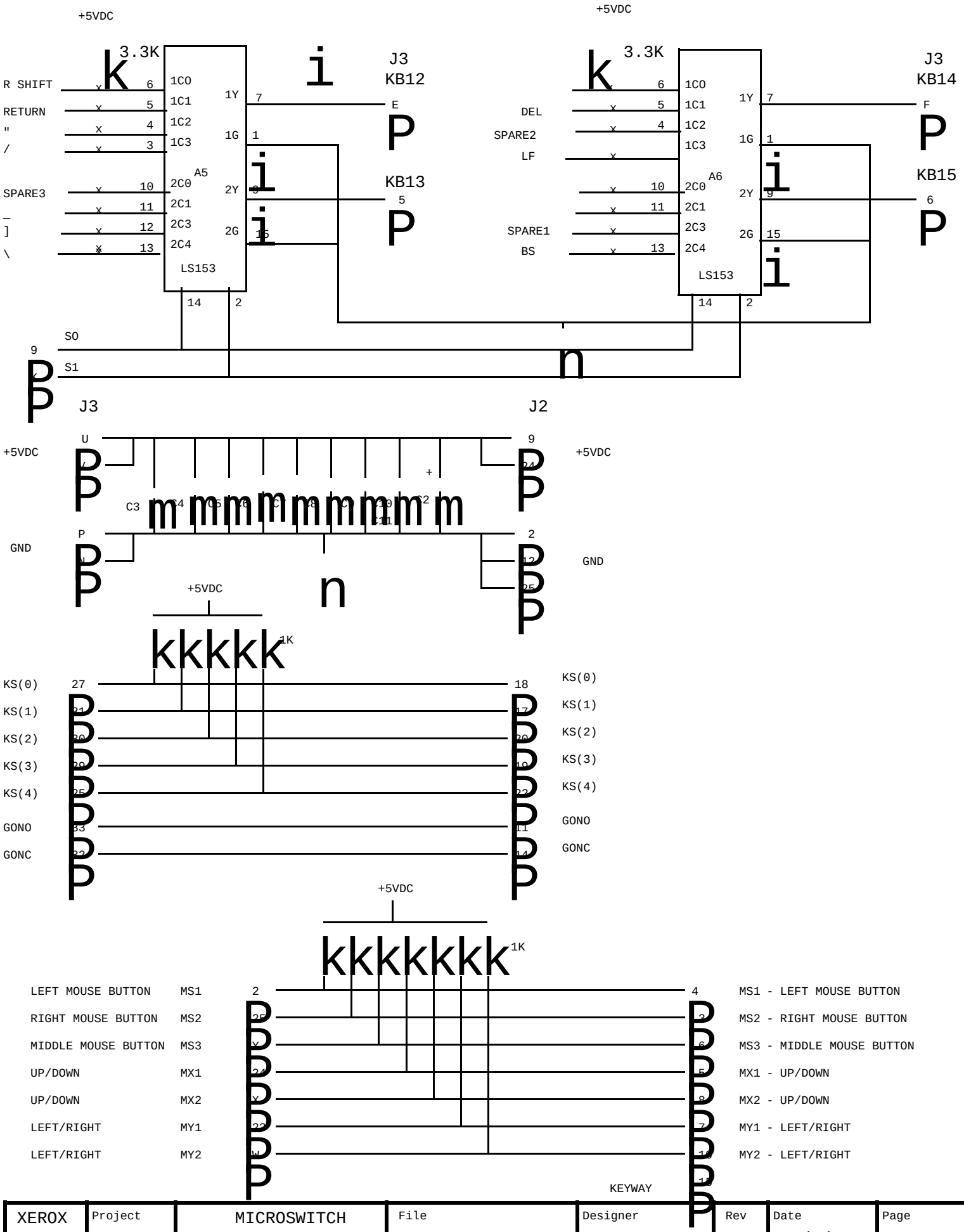
NOTES
1. ■ IS A .01 UF CAP.
2. ▮ IS A .01 UF CAP.

CHANGES TO FIRST BUILD OF THE GARAGE MODEL D0
1. I/C IN LOCATION 2-B PULL PINS #3, 4 & 5 OUT OF THE SOCKET AND WIRE THEM TOGETHER.
2. ADD WIRE FROM I/C IN LOCATION 2-B PINS #3, 4 & 5 TO PIN #8
3. I/C IN LOCATION C-6 ADD WIRE FROM PIN #12 TO PIN #18
4. I/C IN LOCATION C-6 ADD WIRE FROM PIN #21 TO PIN # 24

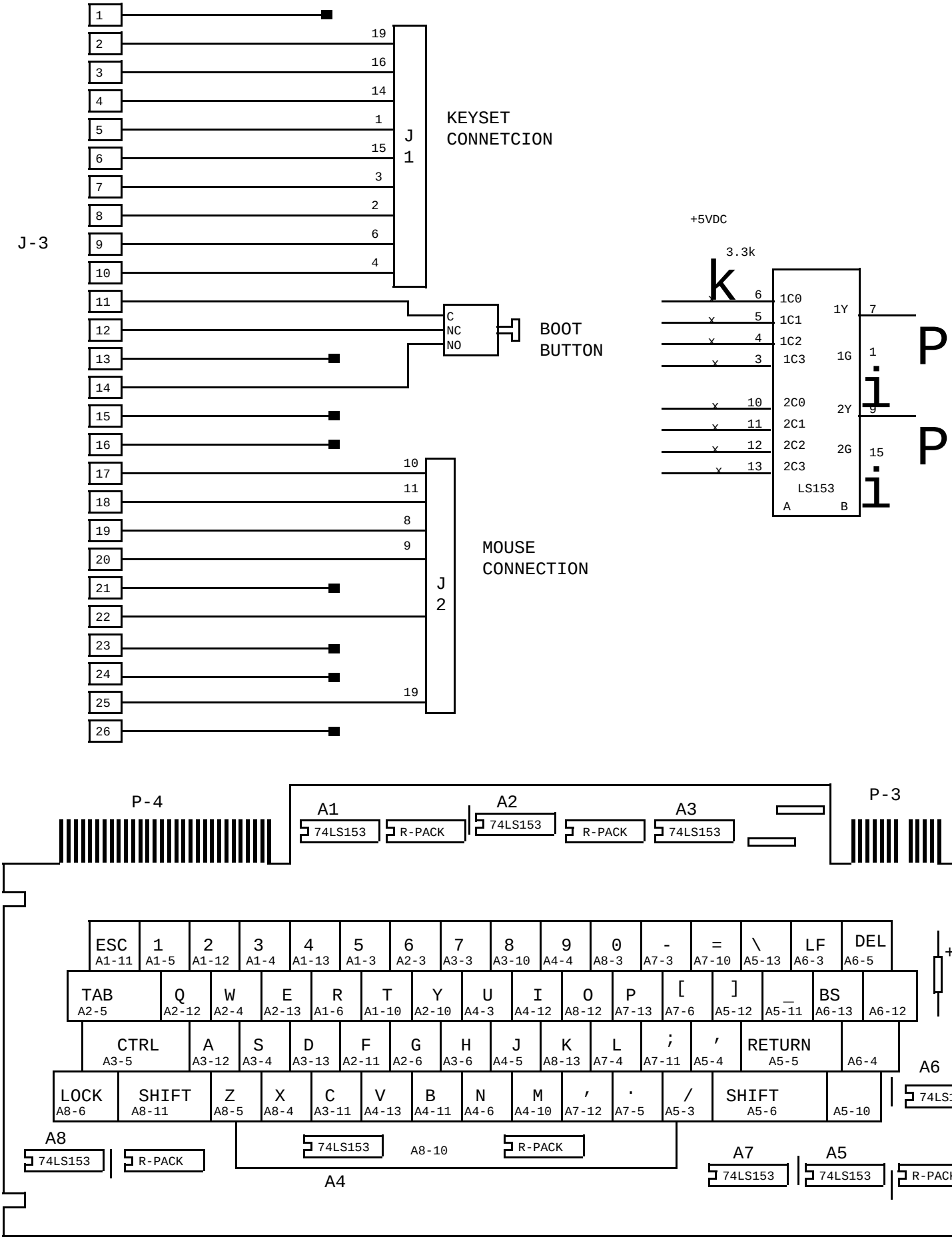
XEROX GARAGE	Project D0	7-WIRE TERMINAL INTERFACE	File IVY<VEST> 7WIRELAYOUT.SIL	Designer VEST	Rev GA	Date 9/26/79	Page 01



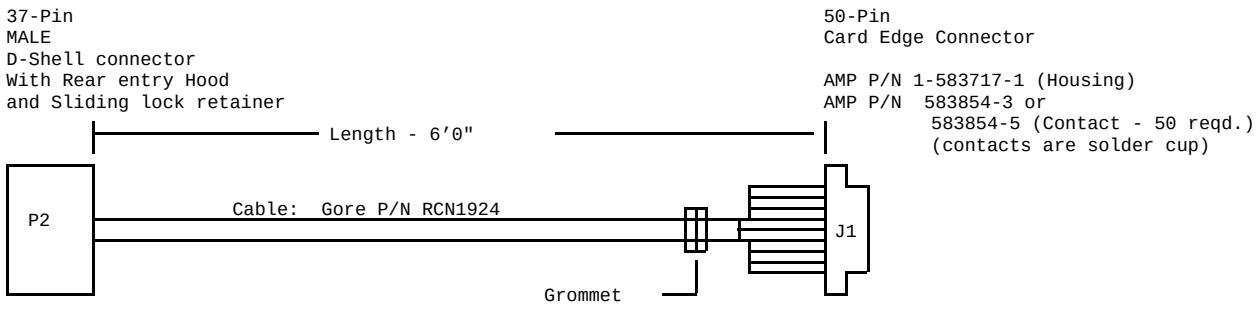
XEROX PARC	Project D0	MICROSWITCH KEYBOARD	File KEYBOARD1.SIL	Designer VEST	Rev A	Date 11/16/80	Page 1



XEROX PARC	Project D0	MICROSWITCH KEYBOARD	File KEYBOARD2.SIL	Designer VEST	Rev A	Date 12/9/80	Page 3



XEROX PARC	Project D0	MICROSWITCH KEYBOARD	File VEST<IVY> KEYBOARDLAYOUT.SIL	Designer VEST	Rev A	Date 11/16/80	Page 1



KEYBOARD J1	7-WIRE INTERFACE P1
1	27 BLU
2	9 Y
3	26 Y/BR
4	13 Y/R
5	14 Y/VIO
6	36 Y/GRY
7	24 Y/BLU
8	5 O
9	28 GRY
13	35 O/VIO
14	7 O/BLK
15	22 O/BLU
16	8 O/BRN
17	31 BLU/BLK
23	12 W/GRN
24	33 W/YEL
25	11 W/BRN
A	4 Y/BLK
B	3 VIO
C	17 GRN
D	18 Y/O
E	37 BRN
F	10 BLK (26 GAUGE-SMALL WIRE)
H	25 R
J	6 Y/GRN
K	29 W (26 GAUGE-SMALL WIRE)
N	2 BLK (20 GAUGE-LARGE WIRE)
P	21 W/VIO
S	23 O/R
T	30 BLU/BRN
U	1 W (20 GAUGE-LARGE WIRE)
V	20 O/GRN
W	32 W/BLU
X	16 W/O
Y	34 W/R
Z	15 W/BLK

KEYBOARD J1	7-WIRE INTERFACE P1
U	1 W (20 GAUGE-LARGE WIRE)
N	2 BLK (20 GAUGE-LARGE WIRE)
B	3 VIO
A	4 Y/BLK
8	5 O
J	6 Y/GRN
14	7 O/BLK
16	8 O/BRN
2	9 Y
F	10 BLK (26 GAUGE-SMALL WIRE)
25	11 W/BRN
23	12 W/GRN
4	13 Y/R
5	14 Y/VIO
Z	15 W/BLK
X	16 W/O
C	17 GRN
D	18 Y/O
V	20 O/GRN
P	21 W/VIO
15	22 O/BLU
S	23 O/R
7	24 Y/BLU
H	25 R
3	26 Y/BR
1	27 BLU
9	28 GRY
K	29 W (26 GAUGE-SMALL WIRE)
T	30 BLU/BRN
17	31 BLU/BLK
W	32 W/BLU
24	33 W/YEL
Y	34 W/R
13	35 O/VIO
6	36 Y/GRY
E	37 BRN

NOTES:

1. CABLE IS 6 FT. LONG +/- 4IN.
2. W/GRY WIRE IS CUT OFF
3. O/GRY WIRE IS CUT OFF
4. CABLE IS (GORE PN#RCN1924)

XEROX CSL	Project GARAGE	EXTERNAL KEYBOARD CABLE	File KEYCABLE.SIL	Designer VEST	Rev A	Date 8/2/79	Page 1