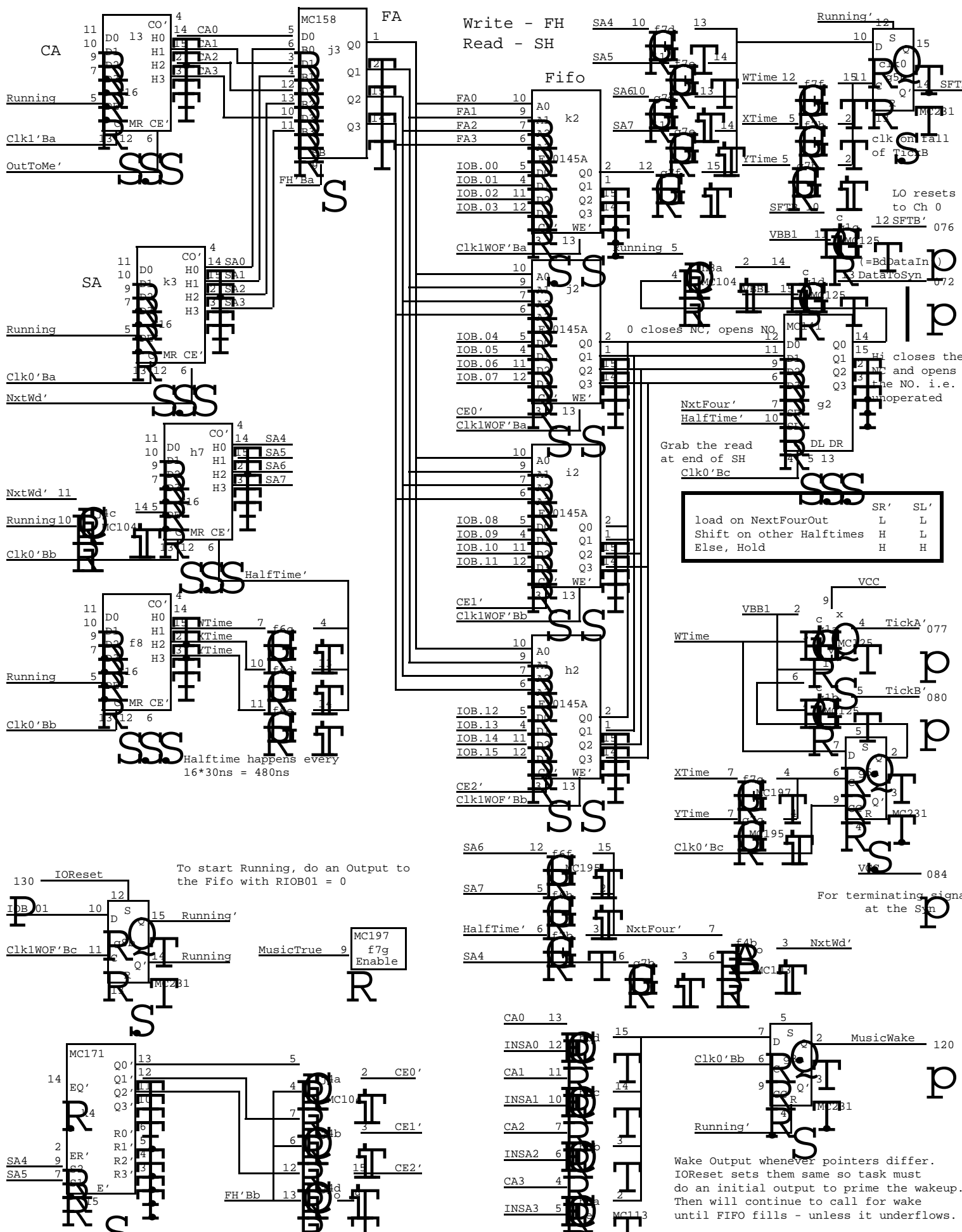


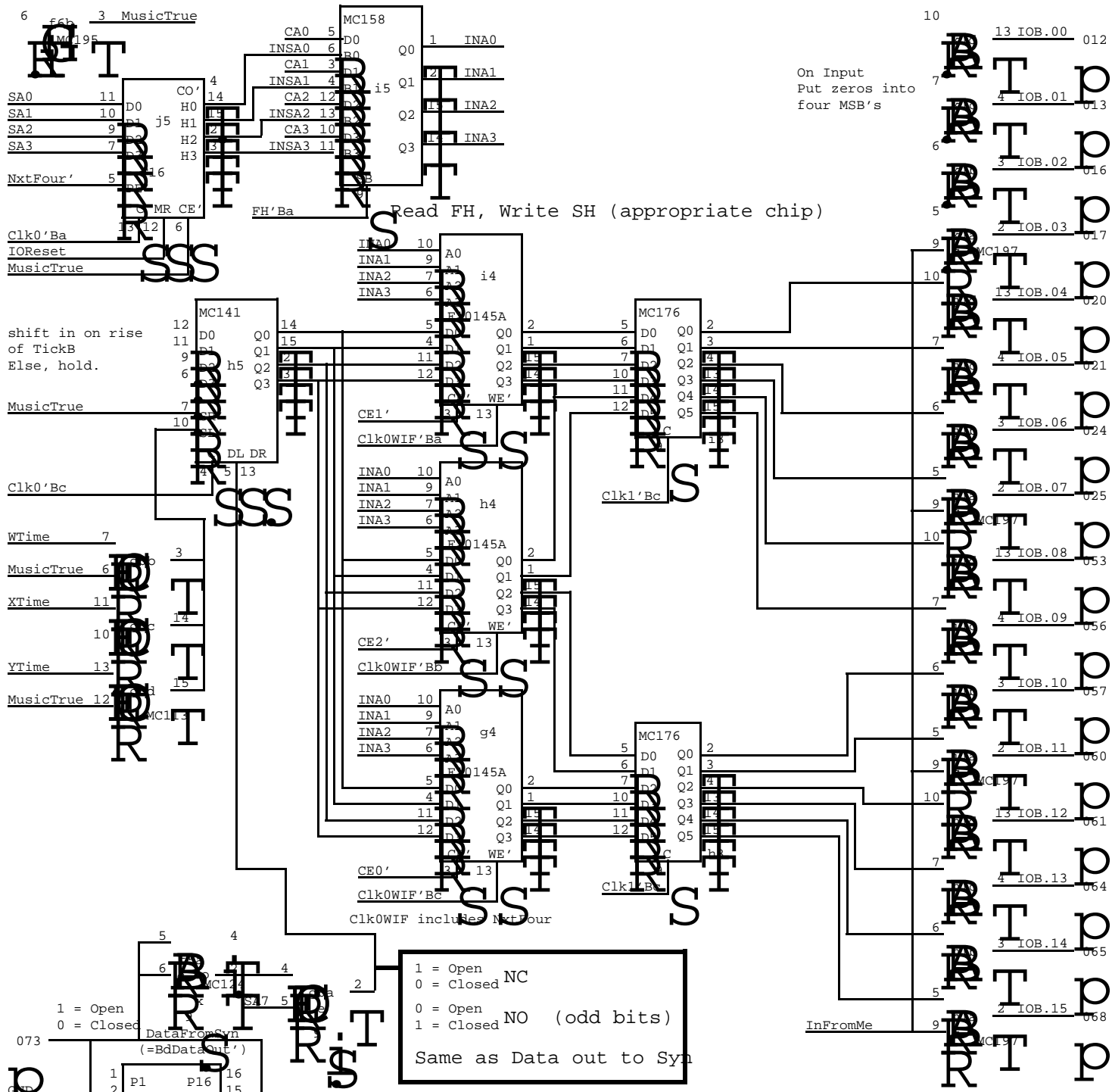
D O R A D O S C H E M A T I C S

M u s i c L o g i c

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On Input
Put zeros into
four MSB's

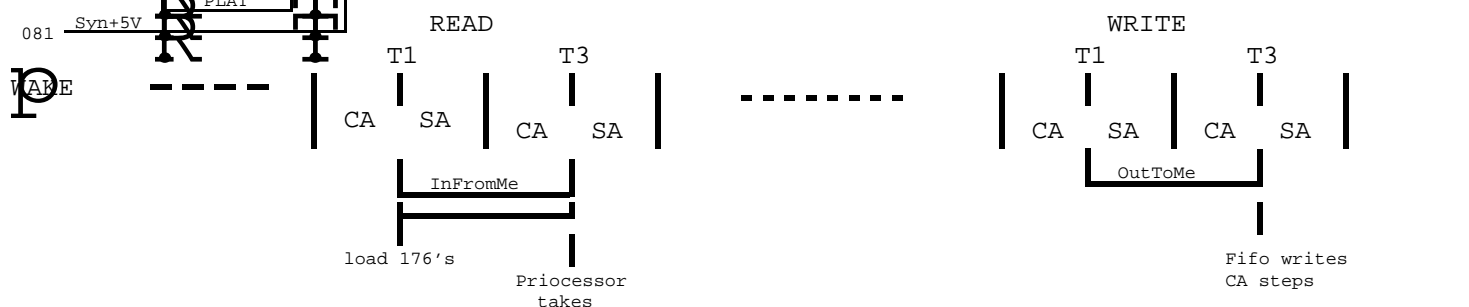
Read FH, Write SH (appropriate chip)

shift in on rise
of TickB
Else, hold.

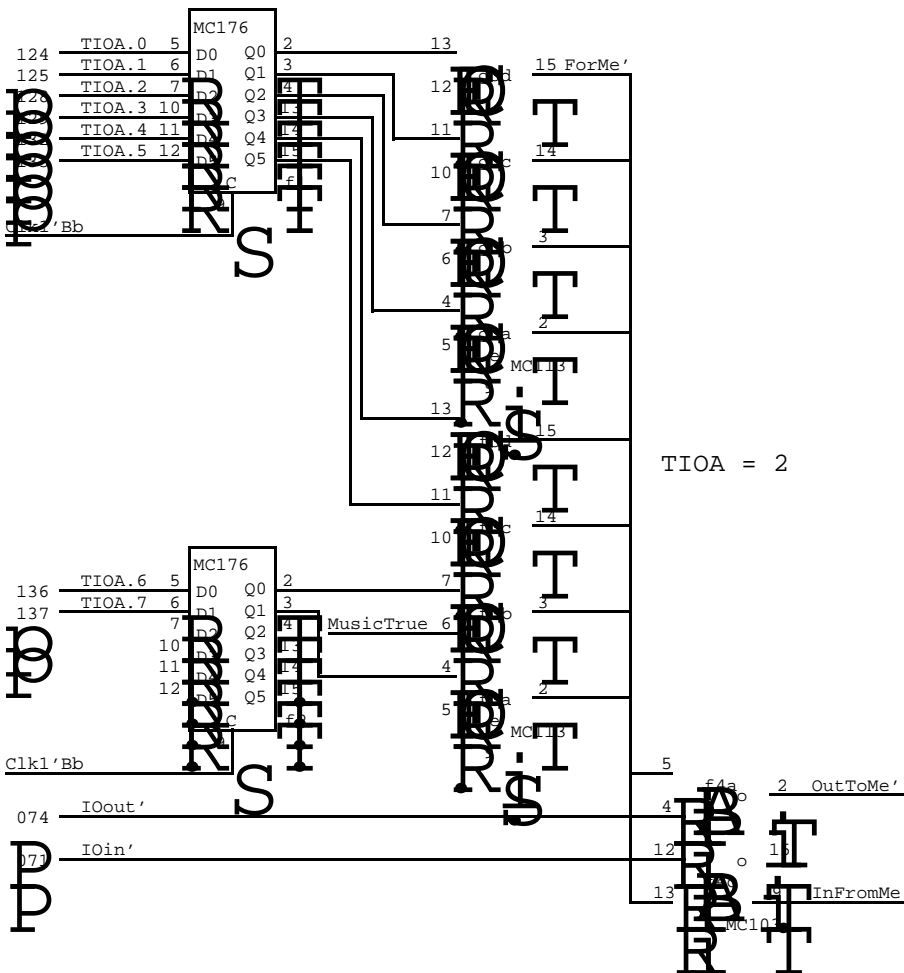
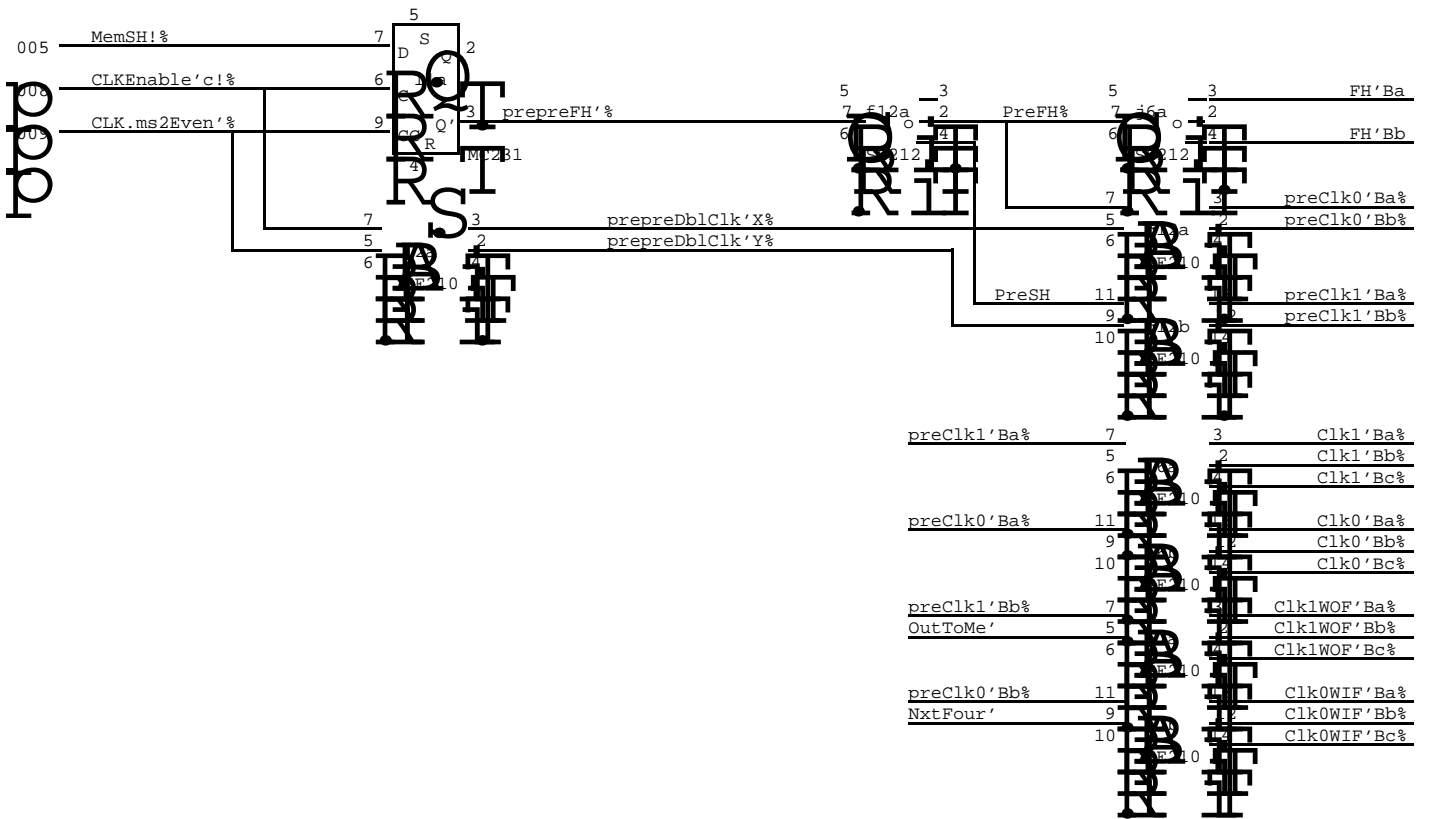
1 = Open NC
0 = Closed
0 = Open NO (odd bits)
1 = Closed
Same as Data out to Syn

P2 - P15: 200 ohms
P3 - P14: 200 ohms
P4 -P13: 6.8 ufd.

Every Wakeup the Microcode first does a Read - which uses CA to access the Input Fifo but followed by a Write to the Output Fifo -which steps CA at the T3 of the Write. Then the mi see if there is another wakeup. Must not block too soon after the Write as the wakeup must

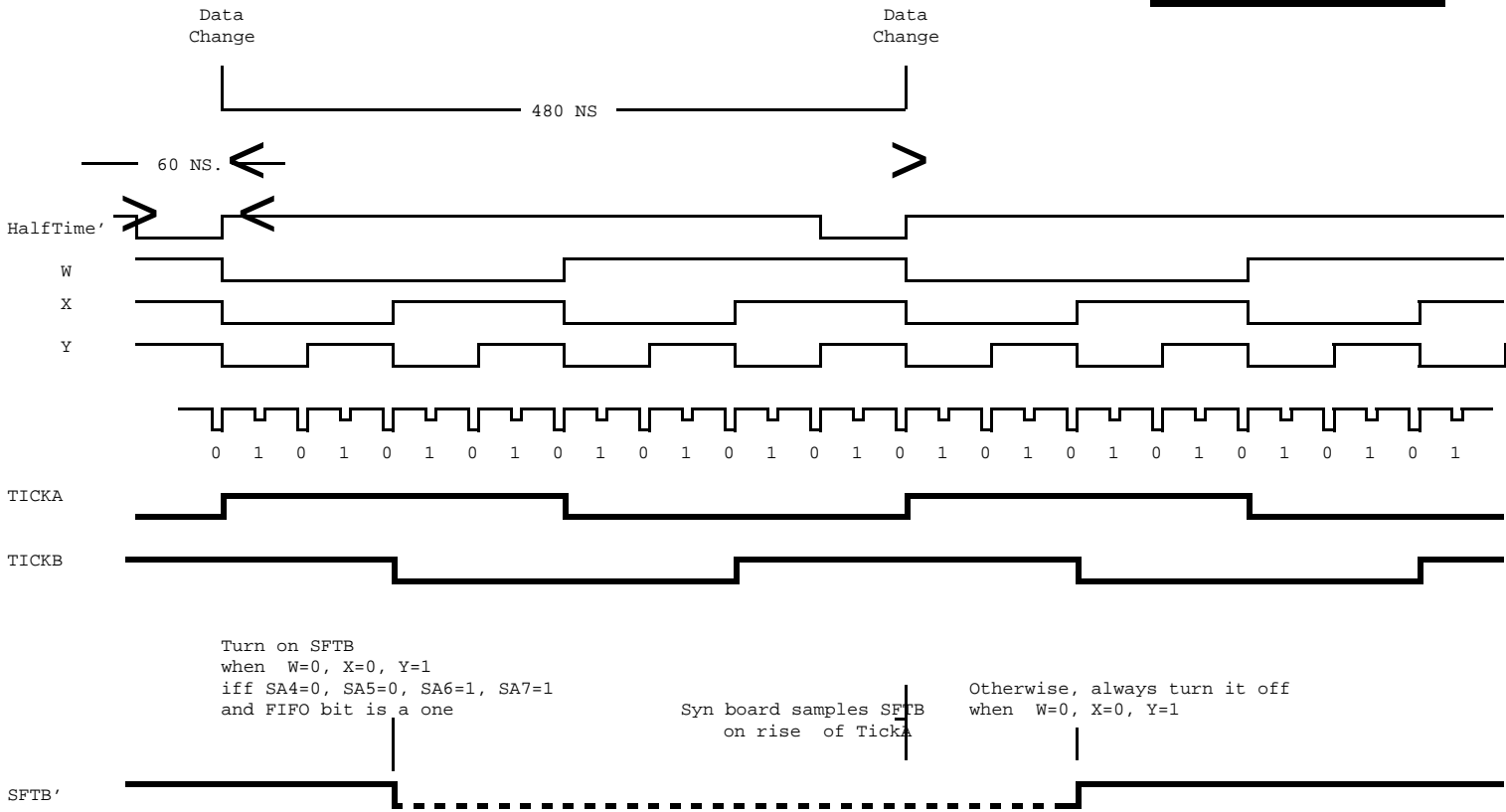


XEROX PARC	Project Music	Input	File Music02.sil	Designer Ornstein	Rev Ac	Date 6/05/80	Page 02
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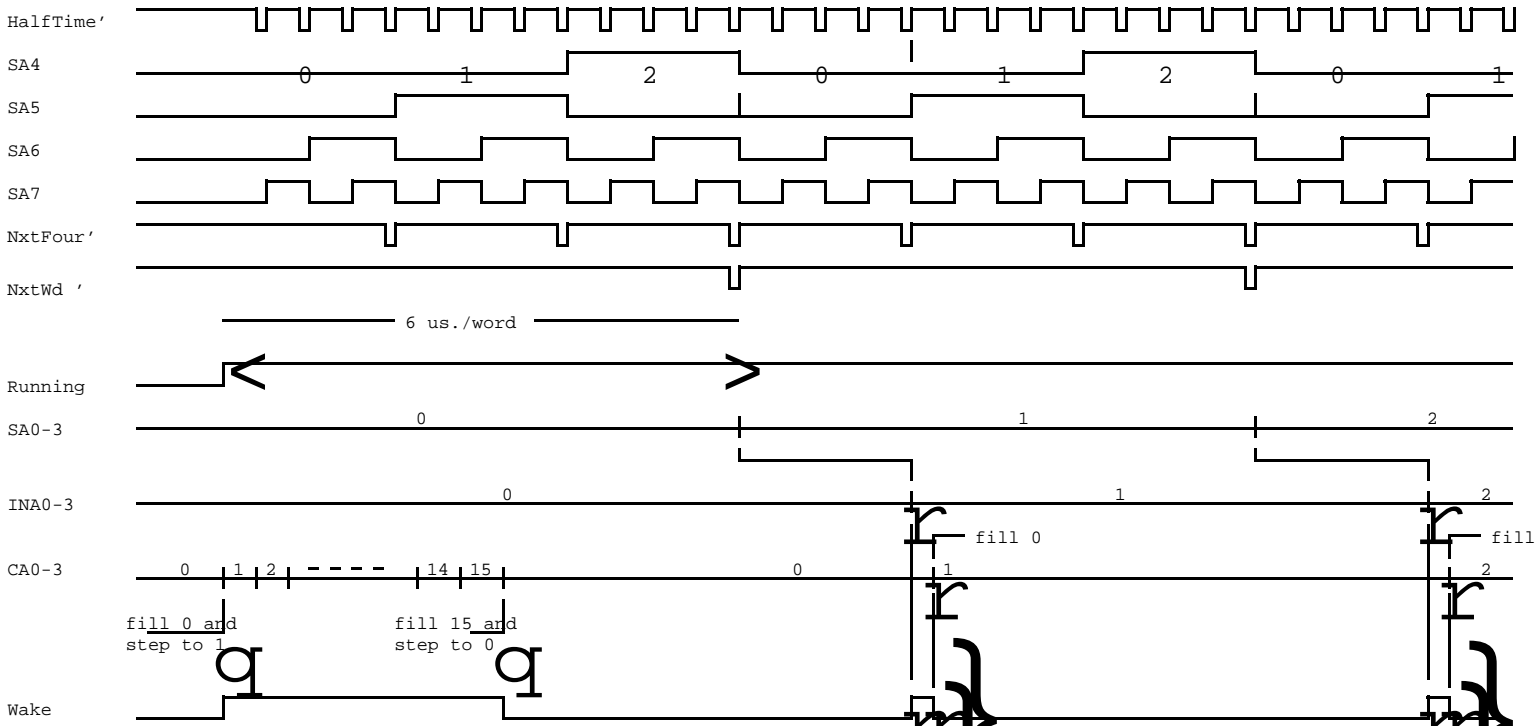


	F	G	H	I	J	K	L	
01	TIOA.0-.5 176	A TickA b TickB c SFTB d DataToSyn 125	IOB Drivers Bits 0-3 197	IOB Drivers Bits 4-7 197	IOB Drivers Bits 8-11 197	IOB Drivers Bits 12-15 197	A prepreFH' b [REDACTED] 231	01
02	TIOA.6-.7 176	Output Shifter 141	Output Fifo Bits 12-15 10145	Output Fifo Bits 8-11 10145	Output Fifo Bits 4-7 10145	Output Fifo Bits 0-3 10145	a prepredbclk' b [REDACTED] 210	02
03	ForMe' Comparator 113	ForMe' Comparator 113	Input Buffer Bits 4-9 176	Input Buffer Bits 10-15 176	OutputFifo Address Mux 158	SA (0-3) F16	CA F16	03
04	a OutToMe' b NxtWd' c InFromMe' d [REDACTED] 103	Input Fifo Bits 4-7 10145	Input Fifo Bits 8-11 10145	Input Fifo Bits 12-15 10145	a CE0' b CE1' c PE SA4-7 D CE2' 104	CE decoder (SA4-5) 171		04
05	A SynDataRcvr b [REDACTED] c [REDACTED] d [REDACTED] 124 312	A TickB b SFTB 231	Input Shifter 141	Input Fifo Address Mux 158	INSA F16	Termination net for DataFromSyn PLAT		05
06	b MusicTrue c HalfTime' d HalfTime' e HalfTime' f NxtFour' h NxtFour' 195	A SynDataFixer b ShiftIn c Timer d 113	CA/INSA Comparator 113	a Clk1' b Clk0' 210	a FH' b [REDACTED] 212			06
07	b NxtFour' c cTickB d dSFTB e dSFTB f cSFTB h cSFTB 197	b SA4' (dNxtWd) c cTickB d dSFTB e dSFTB f dSFTB h cSFTB 195	SA4-7 F16	a ClklWOF' b ClkOWIF' 210				07
08	F16	A MusicWake b Running 231	a DataToSyn b [REDACTED] c [REDACTED] D [REDACTED] 104					08
09								09
10								10
11								11
12	a preFH b [REDACTED] 212	a preClk0' b preClk1' 210						12
13								13

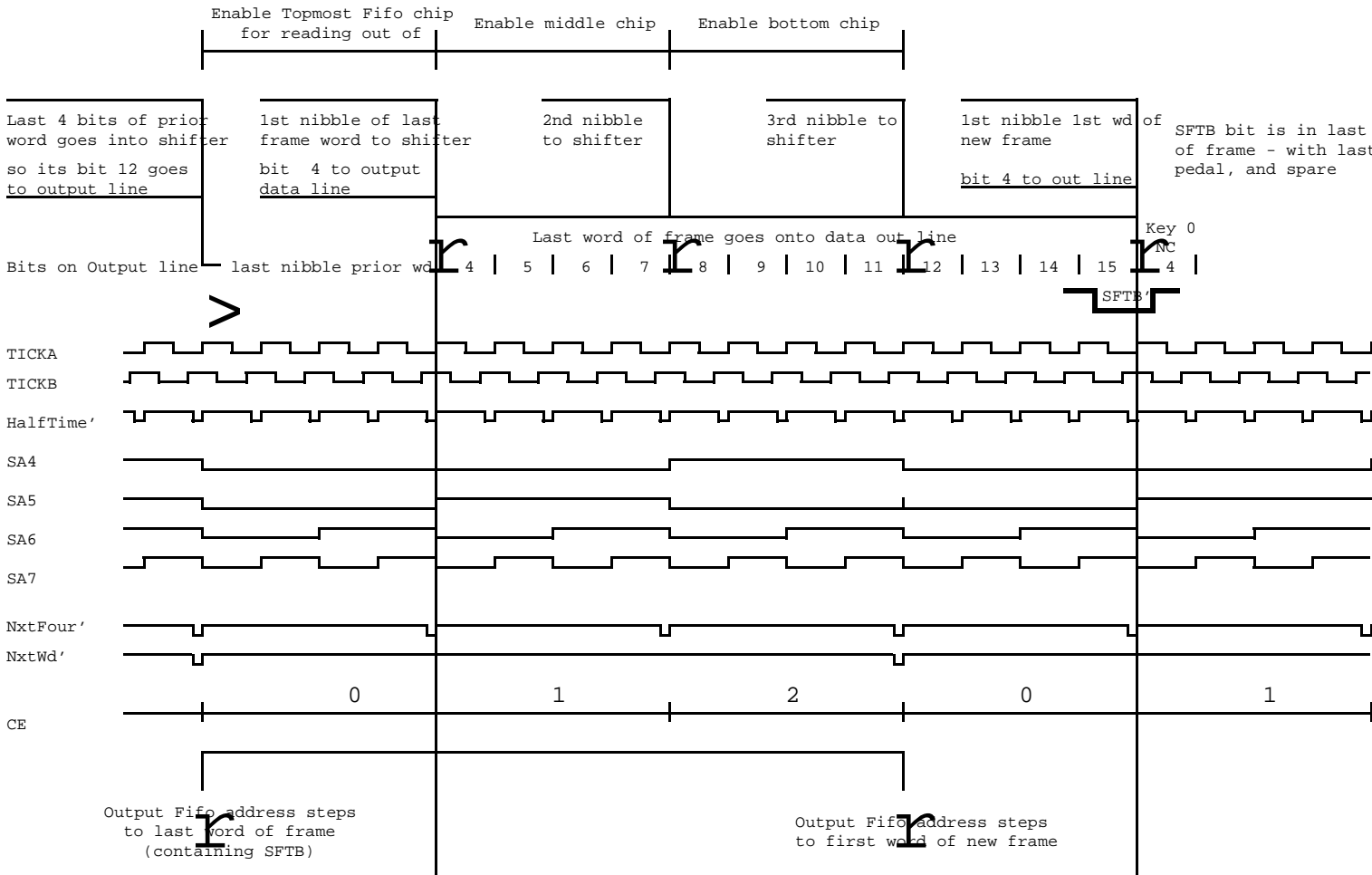
TICKING DETAILS



STARTUP



OUTPUT



INPUT

