D O R A D O S C H E M A T I C S

B a s e B o a r d

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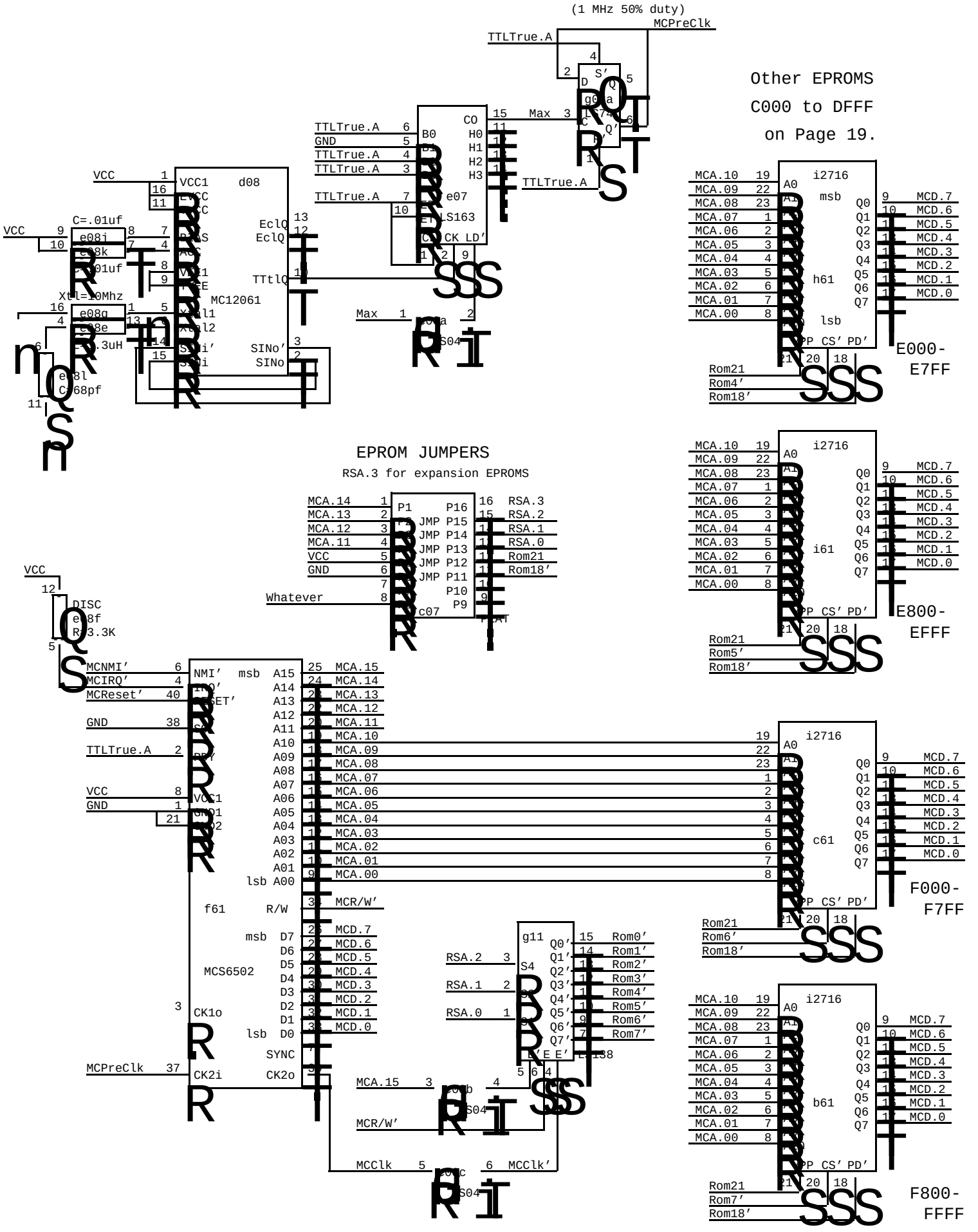
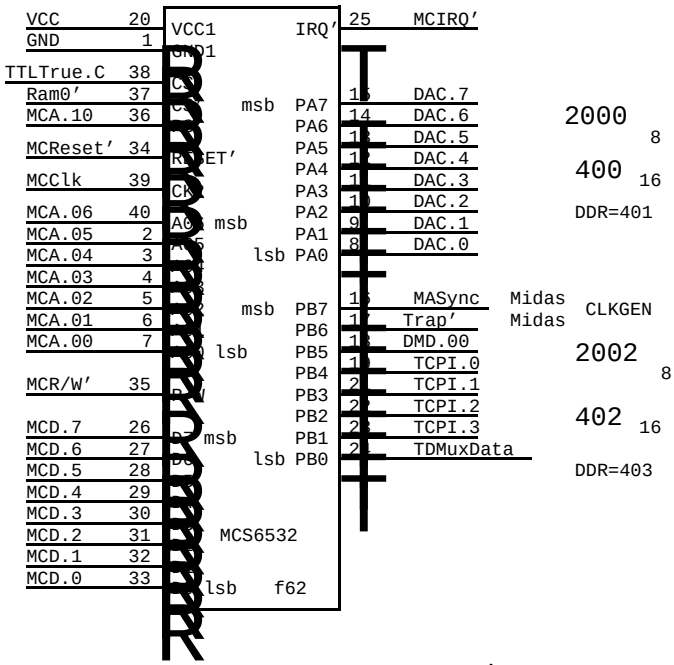
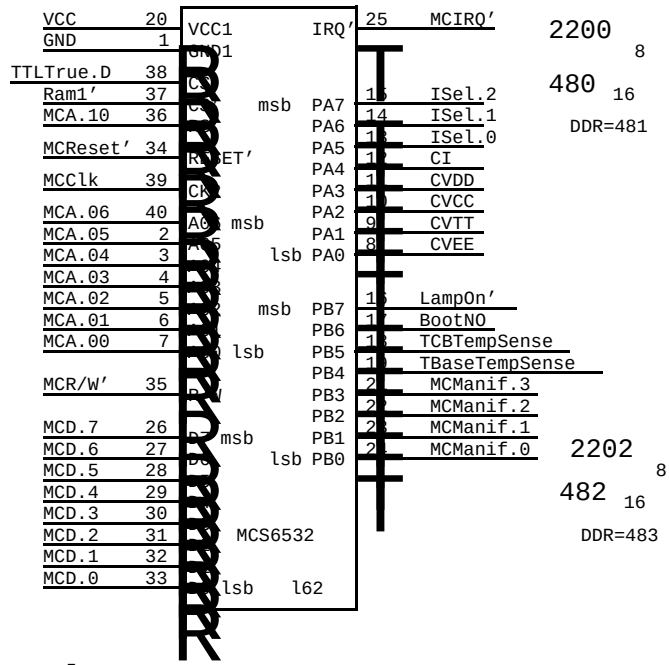
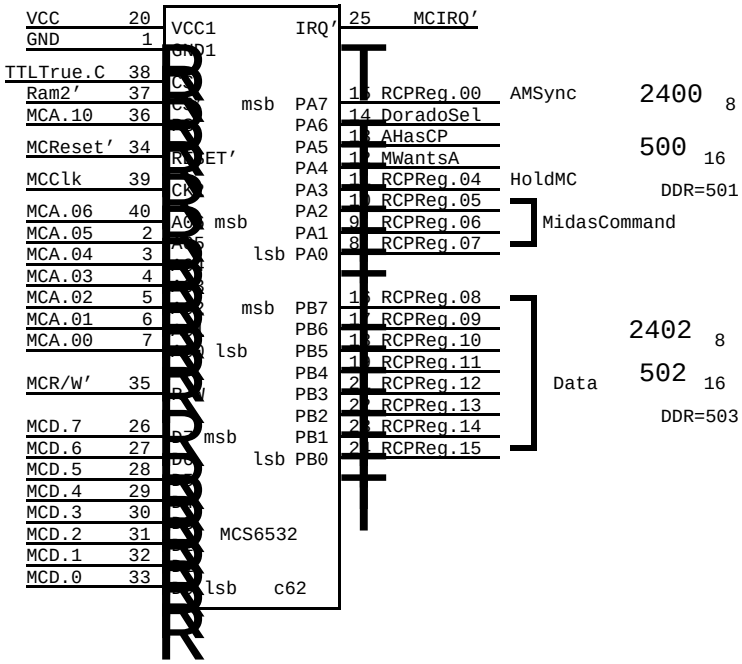


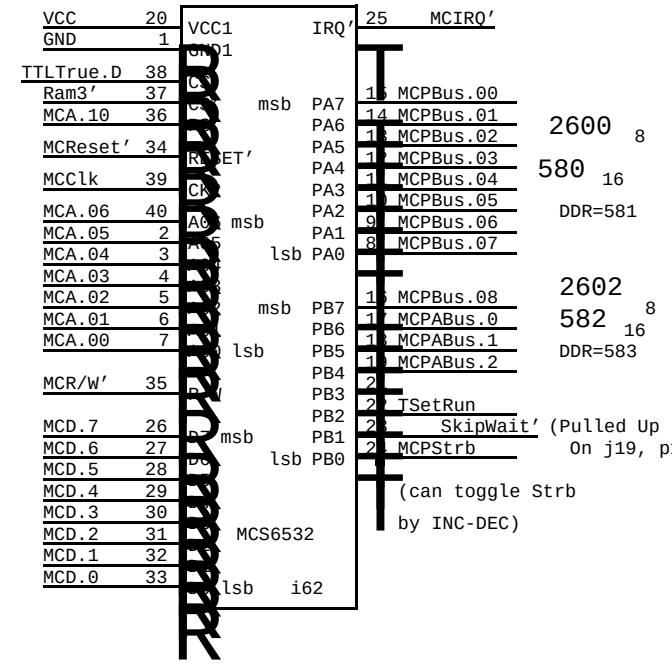
XEROX CSL	Project Dorado	Dorado Base Board Microcomputer and Rom	File Basebd01.si	Designer McCreight	Rev Am	Date 7/27/79	Page 01

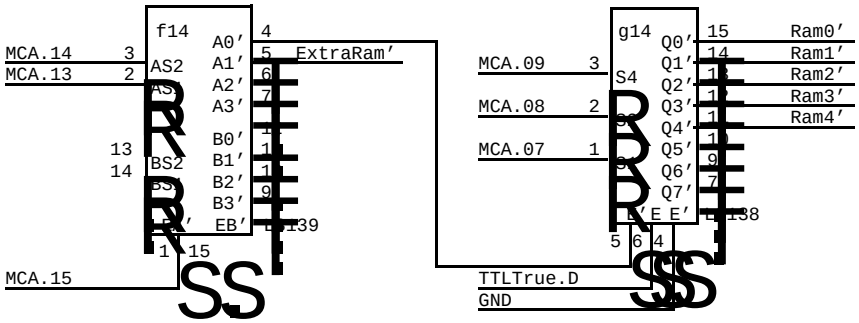




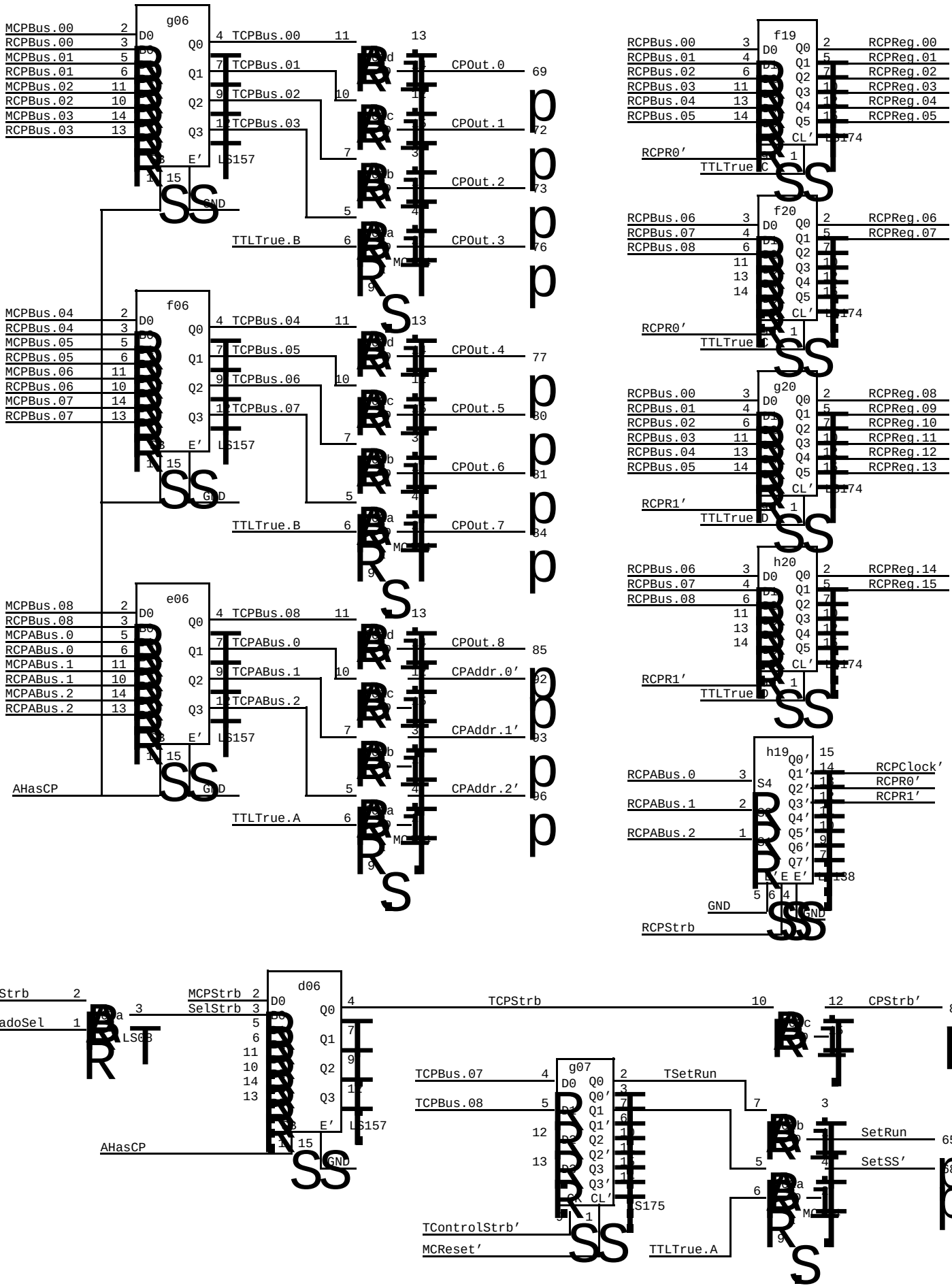
Basic Ram covers[0, 1FF]
16 16



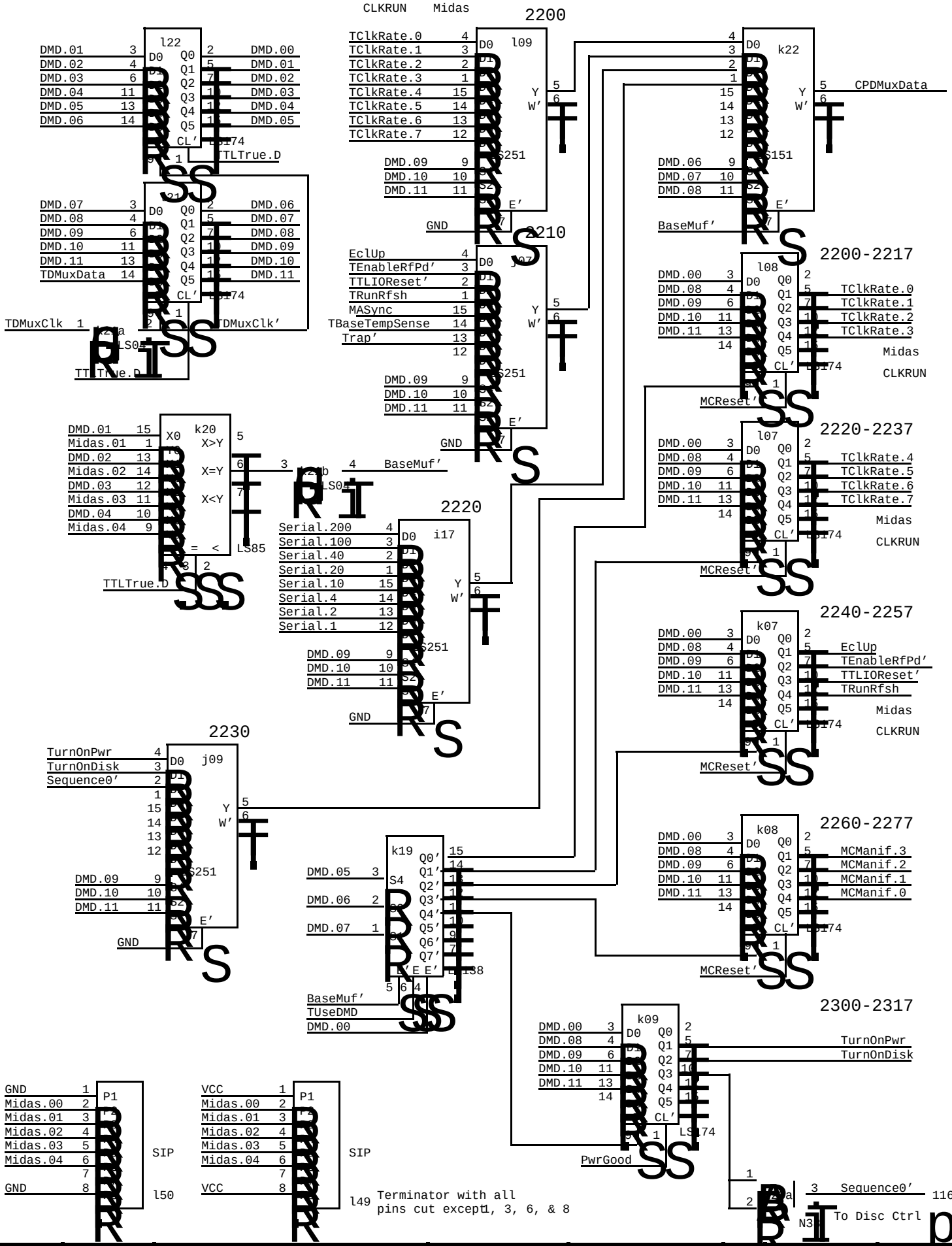




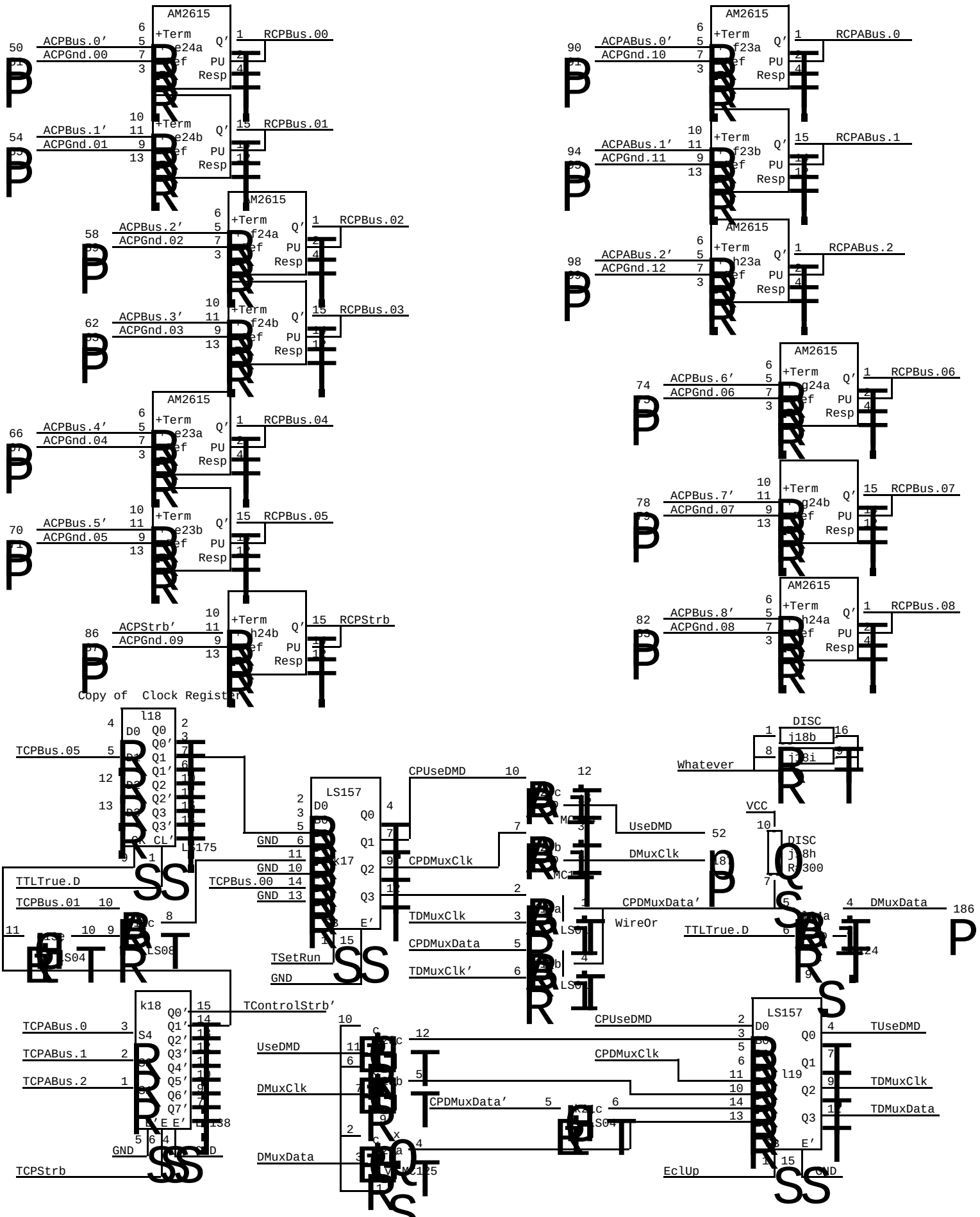
XEROX CSL	Project: Dorado	Dorado Base Board Ram, Timers, and I/O pins	File: Basebd02.si	Designer: McCreight	Rev: Am	Date: 4/29/81	Page: 02



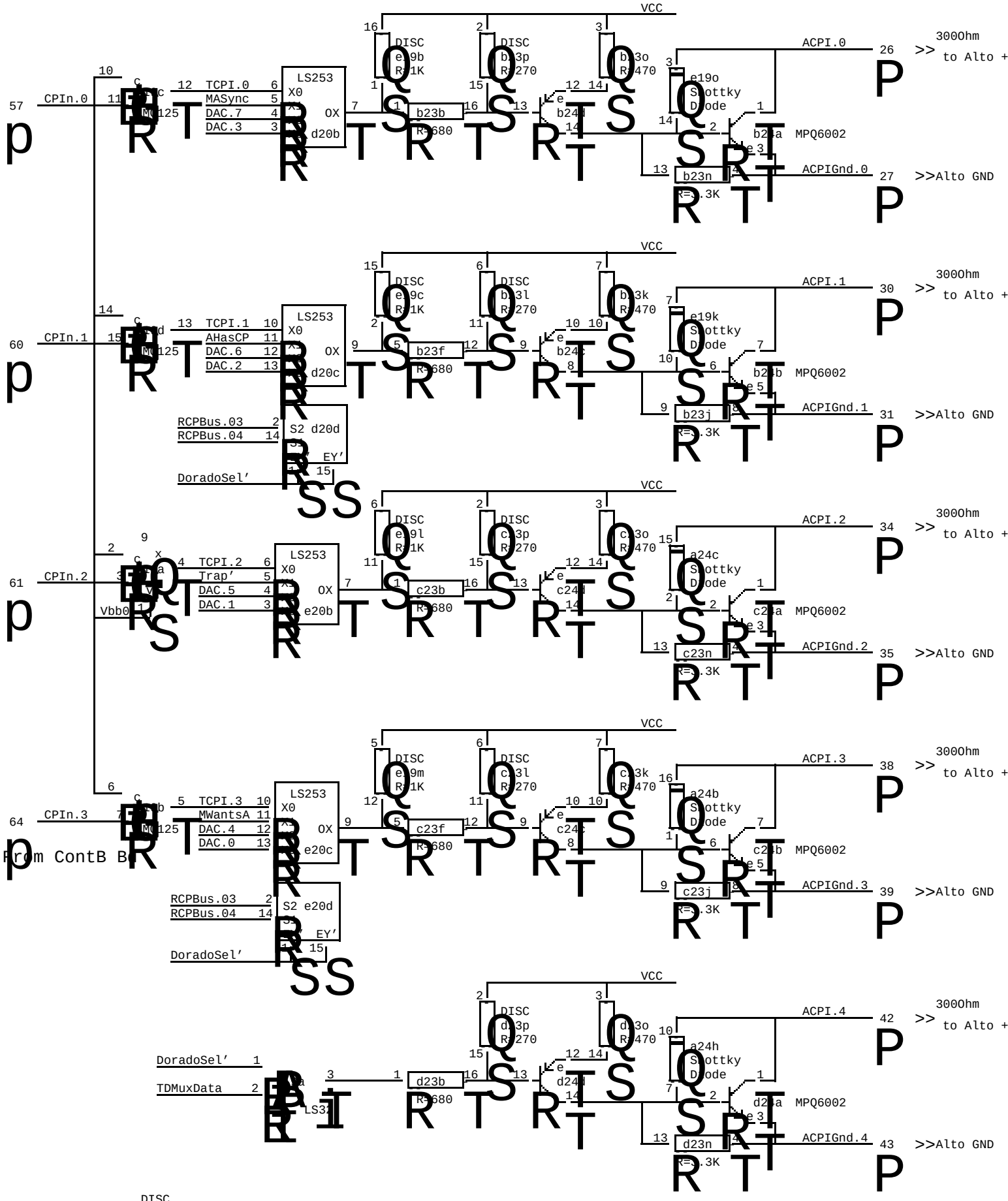
XEROX CSL	Project	Dorado Base Board CP Bus Mpx & ABMux Rcv	File	Designer	Rev	Date	Page
	Dorado		Basebd03.sil	McCreight	Am	7/24/79	03



XEROX CSL	Project: Dorado	Dorado Base Board Muffler and Manifold	File: Basebd04.sil	Designer: McCreight	Rev: Am	Date: 7/24/79	Page: 04

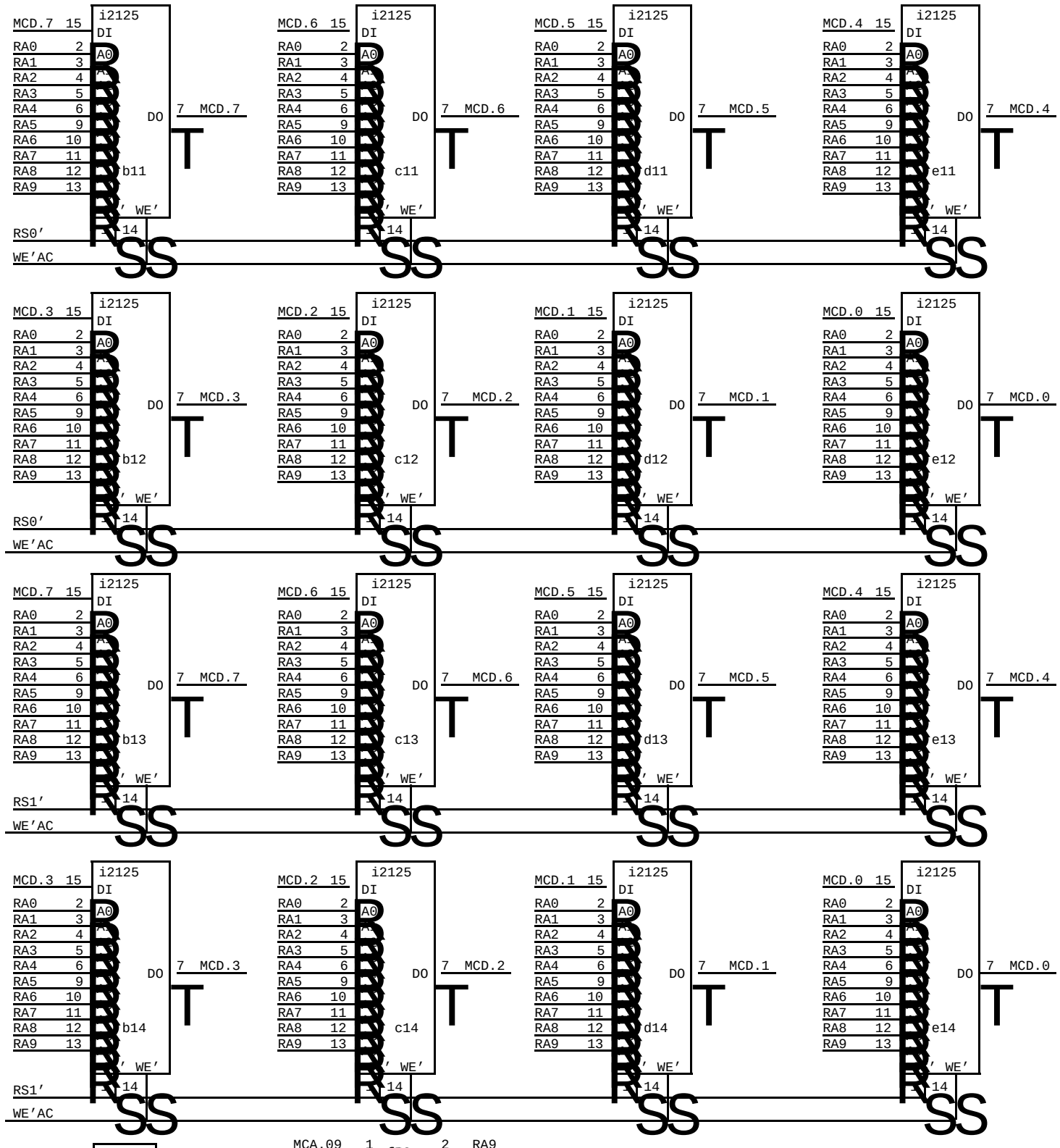


XEROX CSL	Project Dorado	Dorado Base Board Alto Control Bus Receivers	File Basebd05.sil	Designer McCreight	Rev Am	Date 7/25/79	Page 05

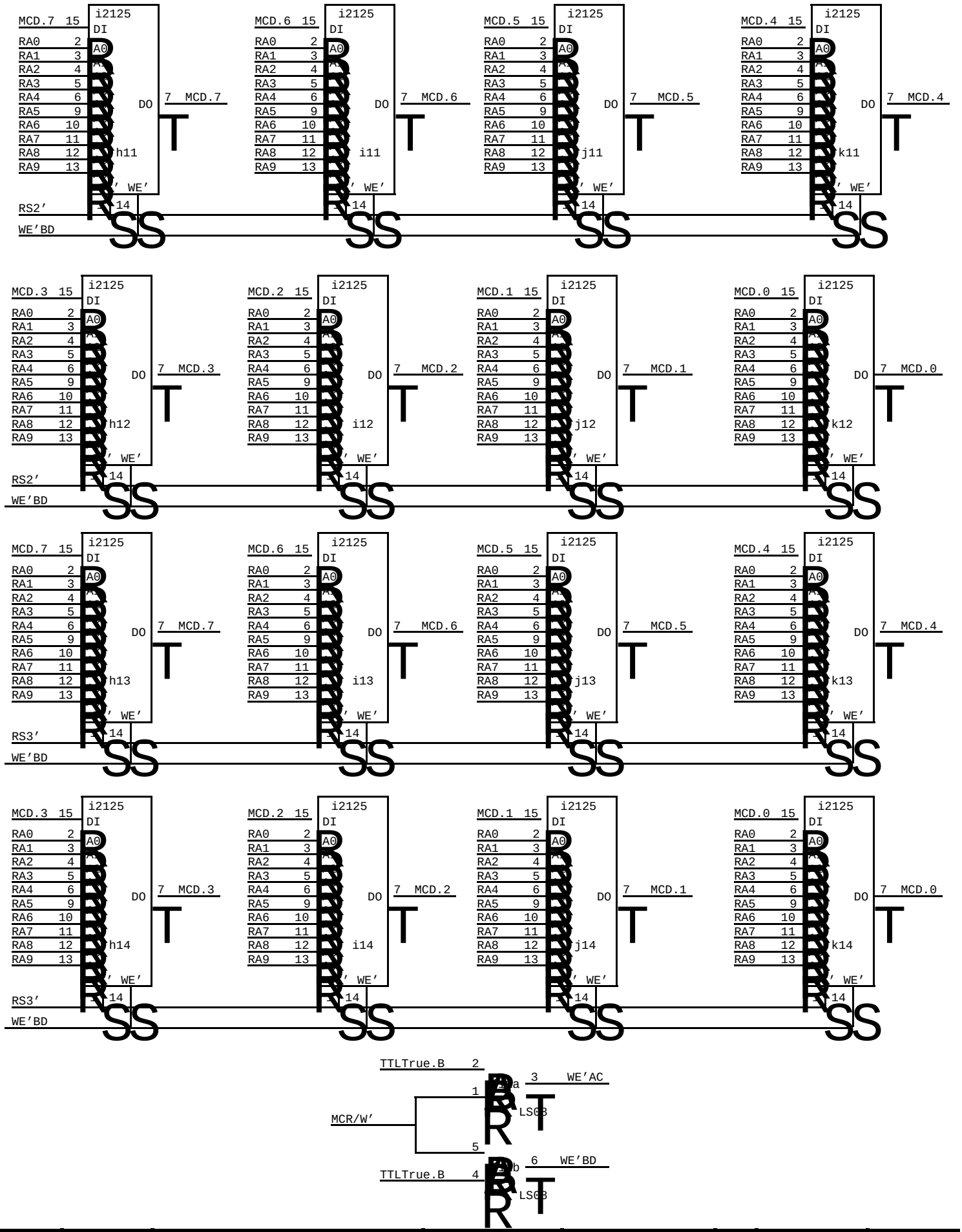


XEROX CSL	Project Dorado	Dorado Base Board Alto Control Bus Drivers	File Basebd06.sil	Designer McCreight	Rev Am	Date 7/27/79	Page 06

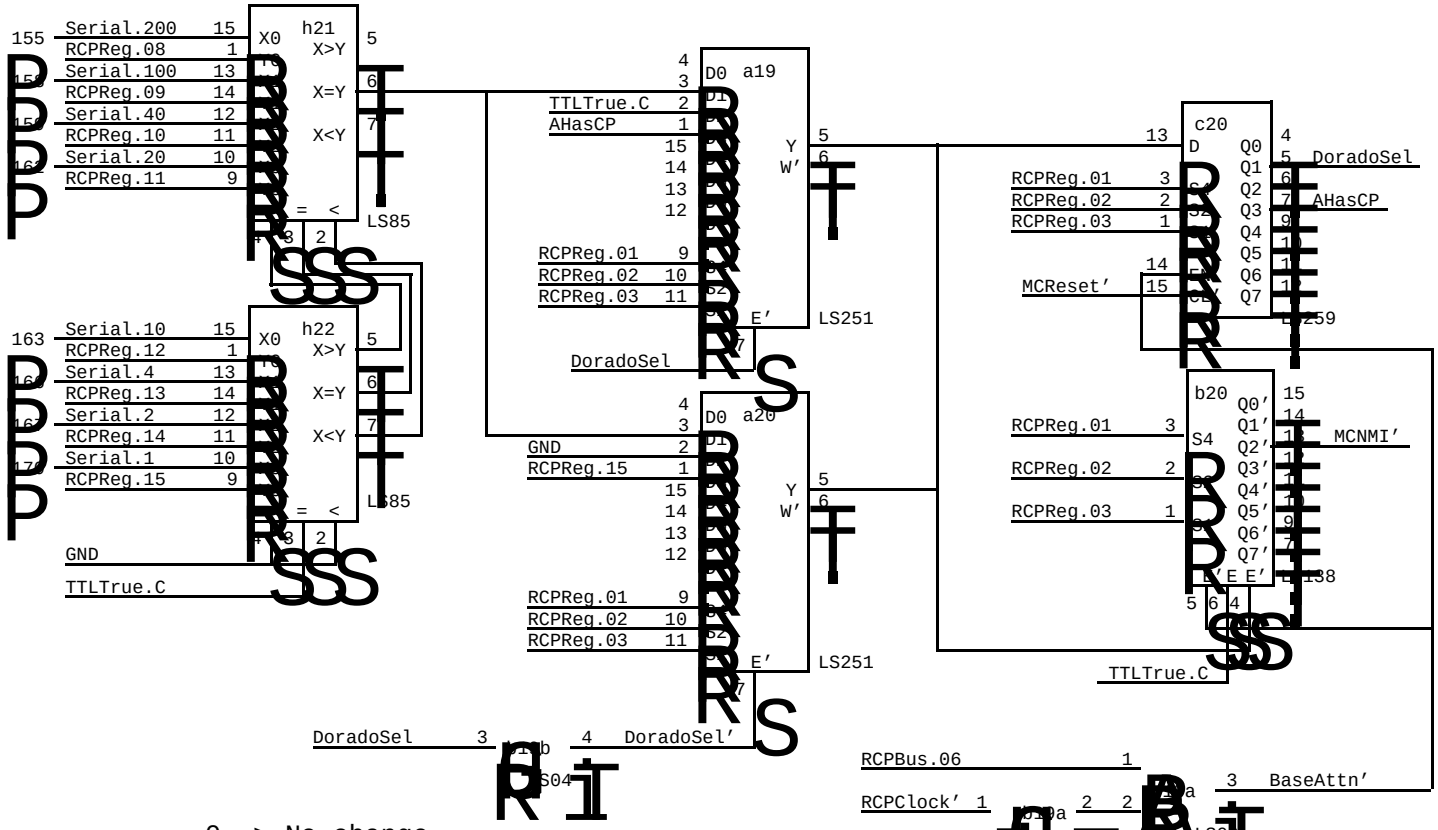
RAMS ARE OPTIONAL



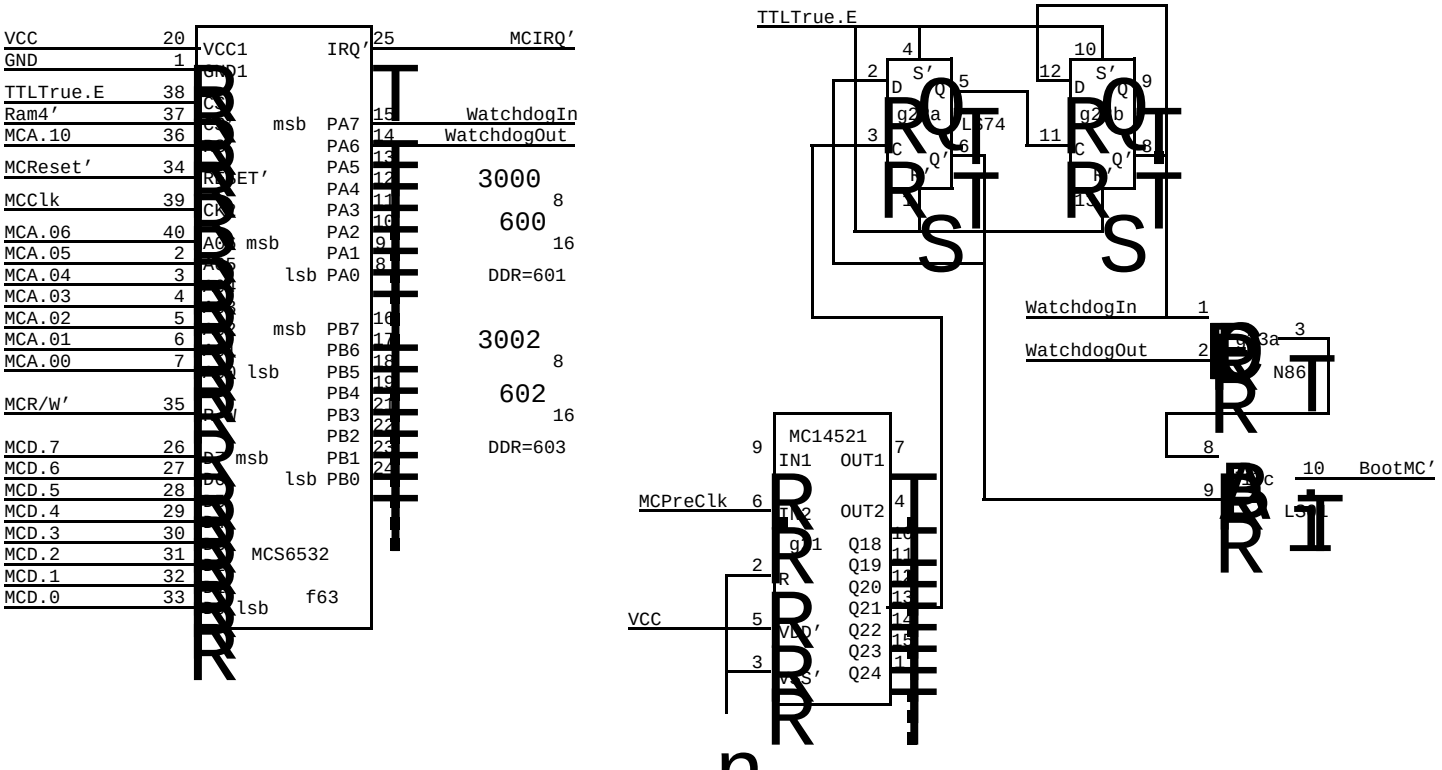
XEROX CSL	Project: Dorado	Dorado Base Board Prototype Ram Module	File: BaseBd07.sil	Designer: McCreight	Rev: Am	Date: 7/24/79	Page: 07



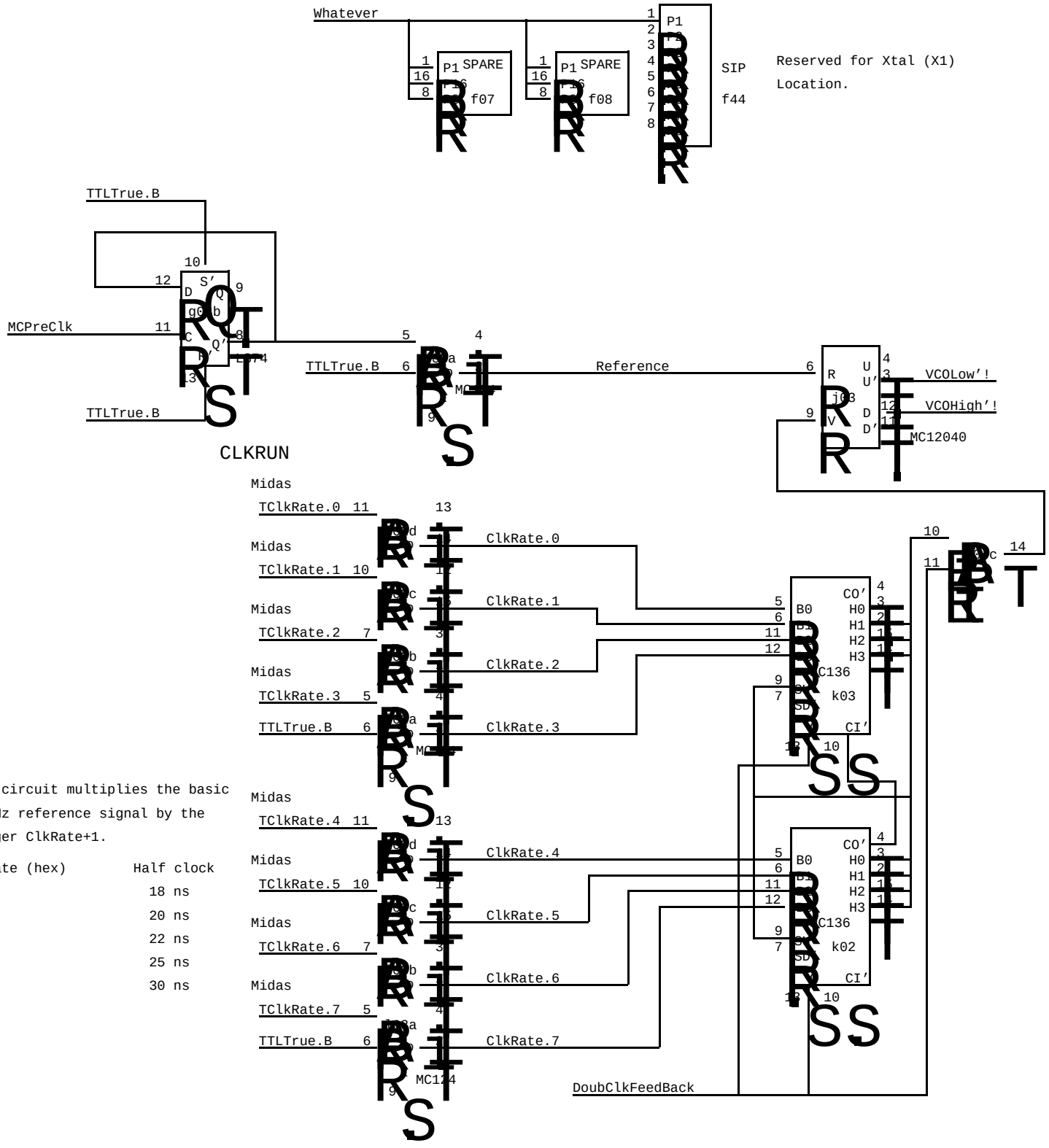
XEROX CSL	Project Dorado	Dorado Base Board Prototype Ram Module	File BaseBd08.sil	Designer McCreight	Rev Am	Date 7/24/79	Page 08



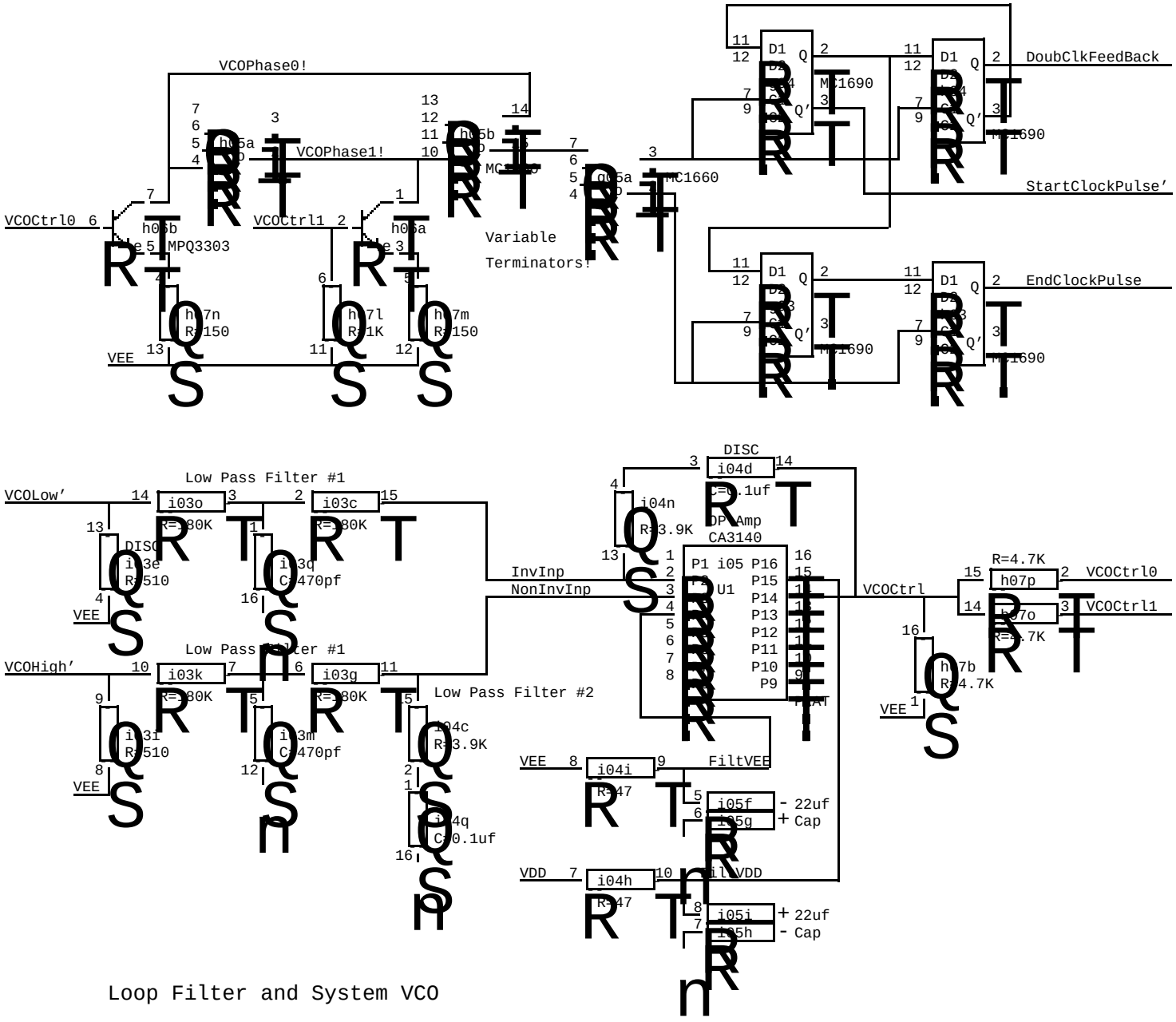
0 => No change
1 => Select Dorado from RCPReg[8-15]
2 => Break microcomputer if Dorado is selected
3 => CPBus control _ If Dorado is selected:
If RCPReg.15 = 1, Alto has Bus
If RCPReg.15 = 0, Microcomputer has Bus
Else No Change



XEROX CSL	Project: Dorado	CP Bus Handoff and Watchdog Timer	File: BaseBd09.sil	Designer: McCreight	Rev: Am	Date: 4/30/81	Page: 01

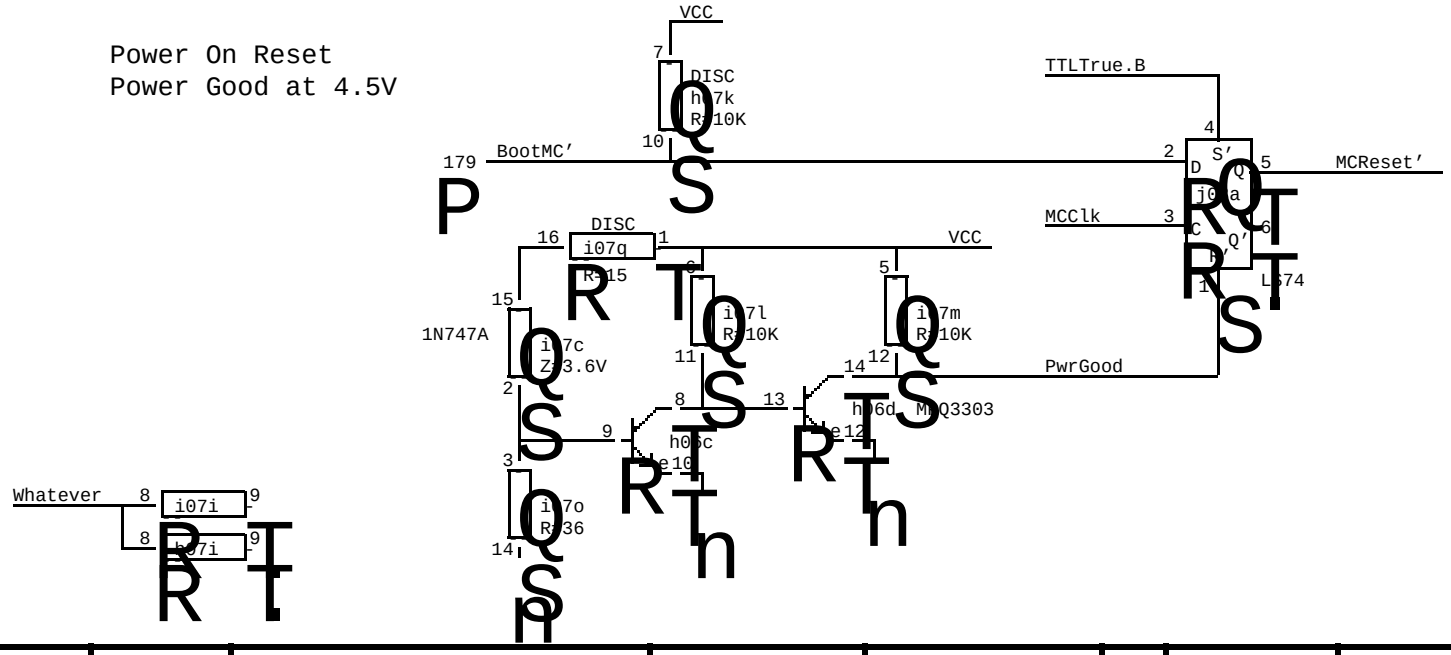


XEROX CSL	Project Dorado	Clock Generator Reference & Freq Comp	File BaseBd10.sil	Designer McCreight	Rev Am	Date 4/29/81	Page 10

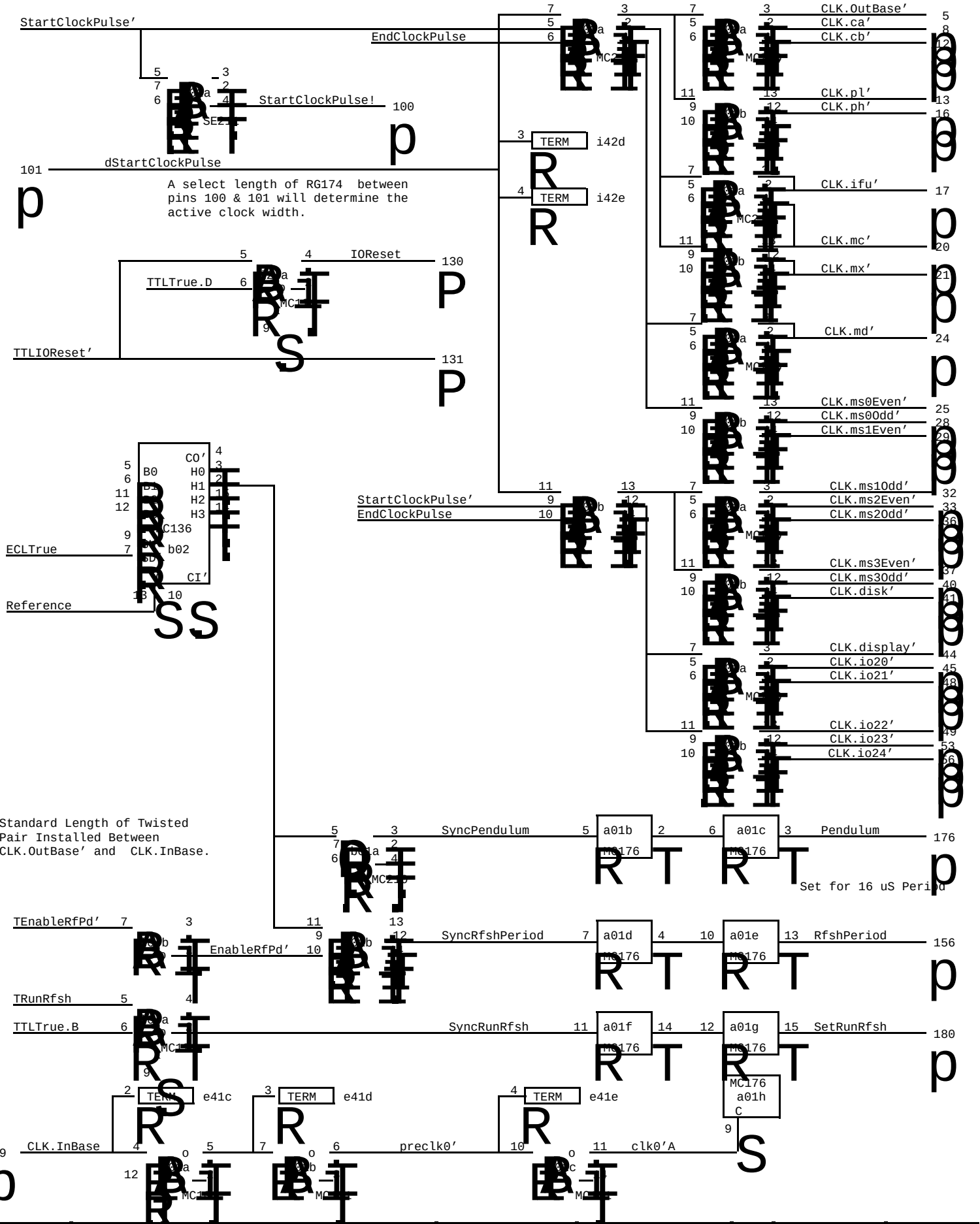


Loop Filter and System VCO

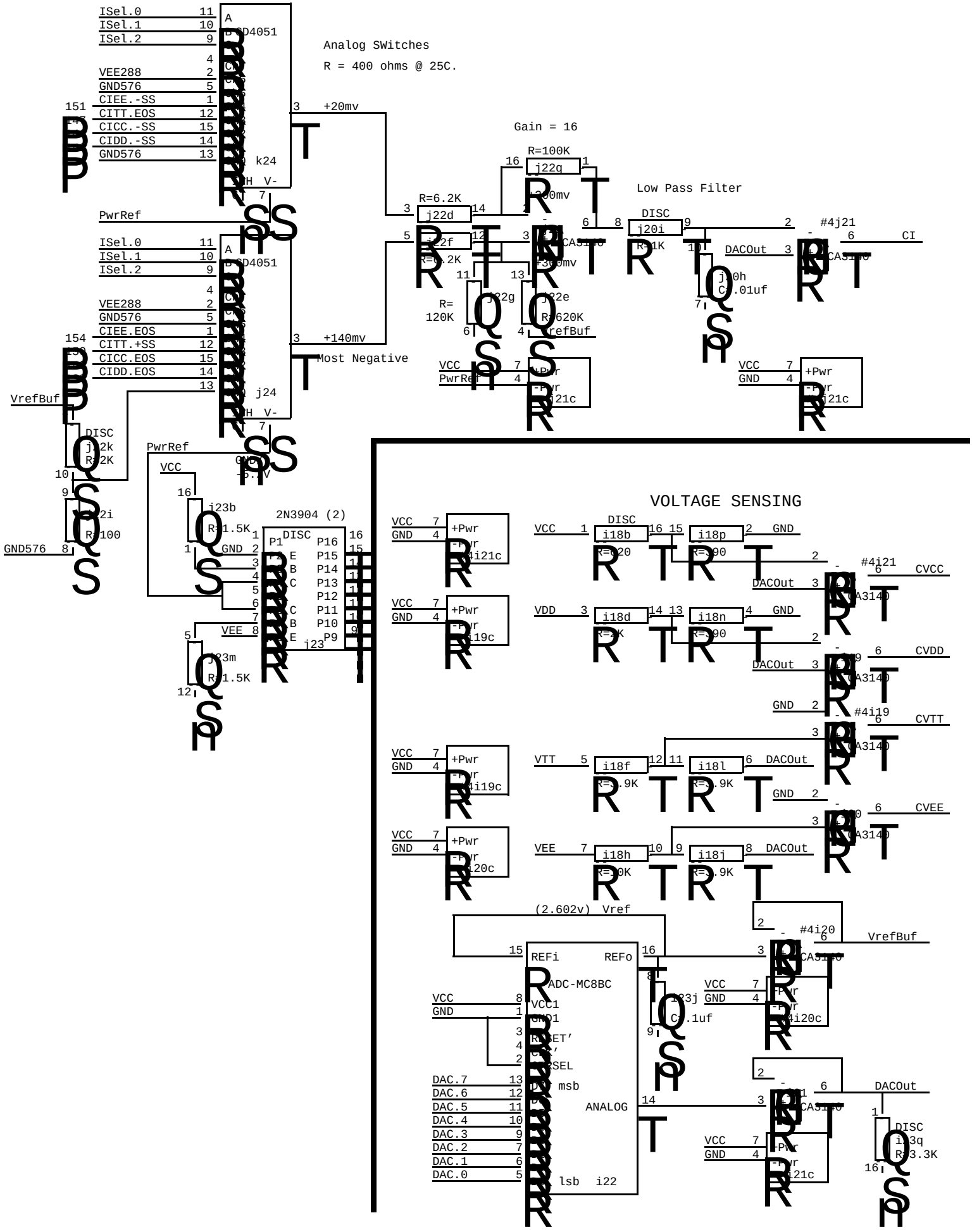
Power On Reset
Power Good at 4.5V



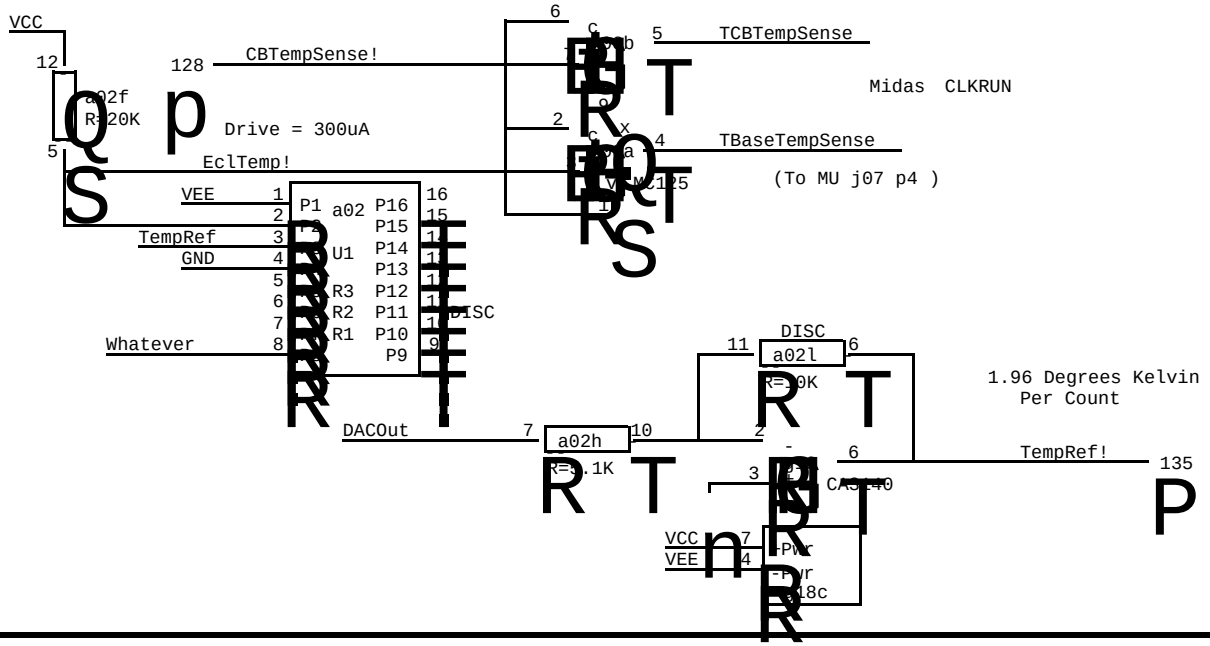
XEROX CSL	Project Dorado	Clock Generator Loop filter and VCO	File BaseBd11.sil	Designer McCreight	Rev Am	Date 7/31/79	Page 11

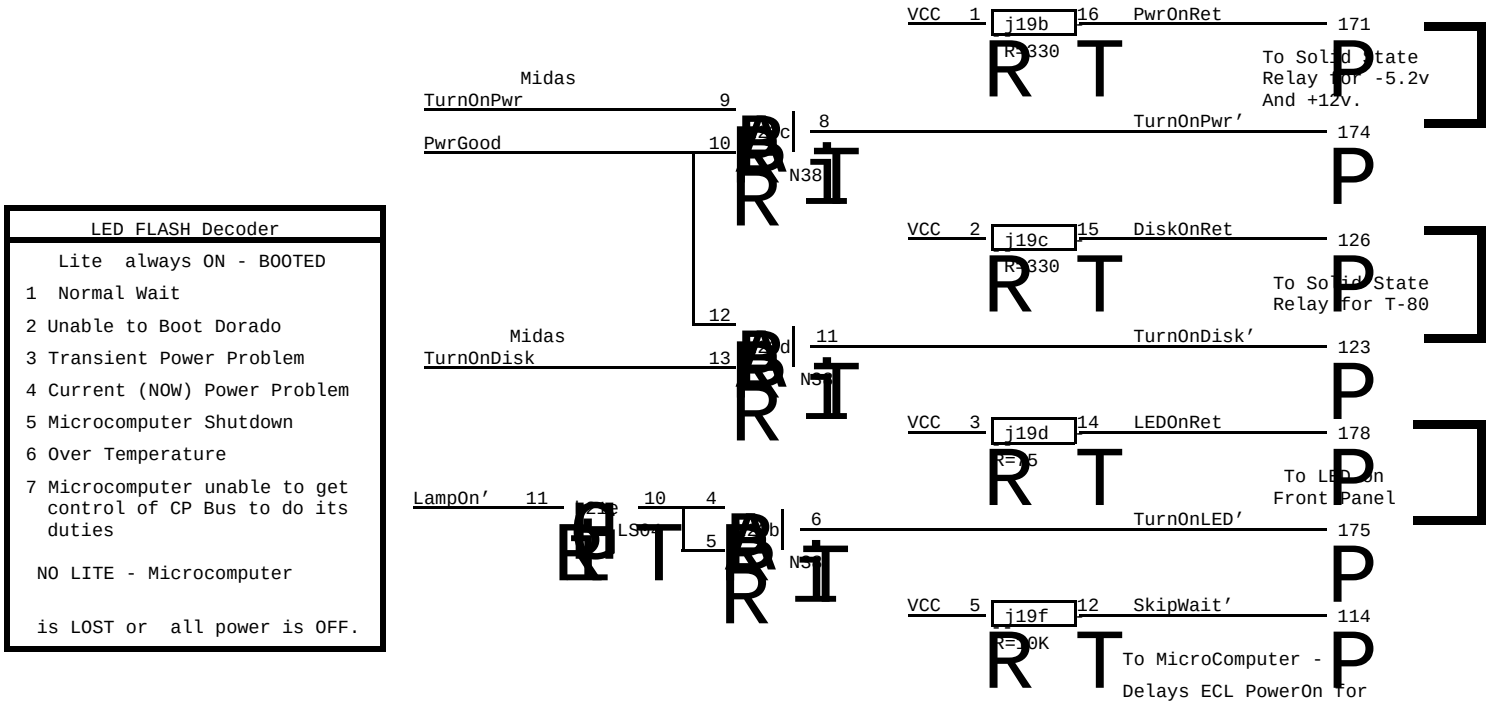


XEROX CSL	Project: Dorado	Clock Generator Backpanel distributor	File: BaseBd12.sil	Designer: McCreight	Rev: Am	Date: 7/25/79	Page: 12



XEROX CSL	Project	Dorado Base Board Analog/Digital Conversion	File	Designer	Rev	Date	Page
	Dorado		BaseBd13.sil	McCreight	Am	7/27/79	13





LED FLASH Decoder

Lite always ON - BOOTED

1 Normal Wait

2 Unable to Boot Dorado

3 Transient Power Problem

4 Current (NOW) Power Problem

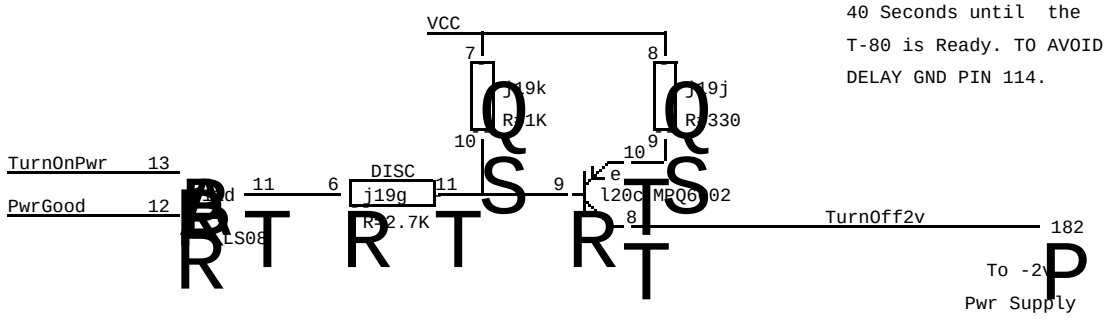
5 Microcomputer Shutdown

6 Over Temperature

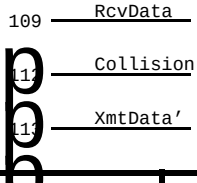
7 Microcomputer unable to get control of CP Bus to do its duties

NO LITE - Microcomputer

is LOST or all power is OFF.

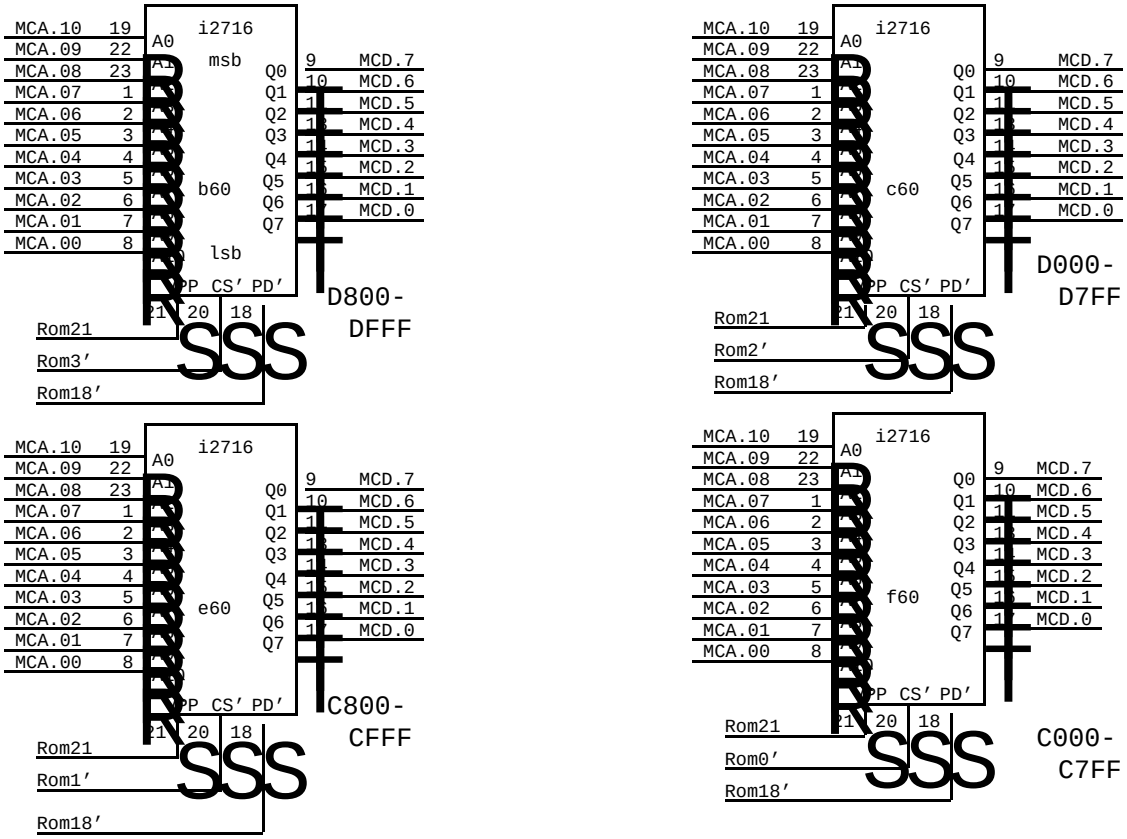


For Future Baseboard Ethernet

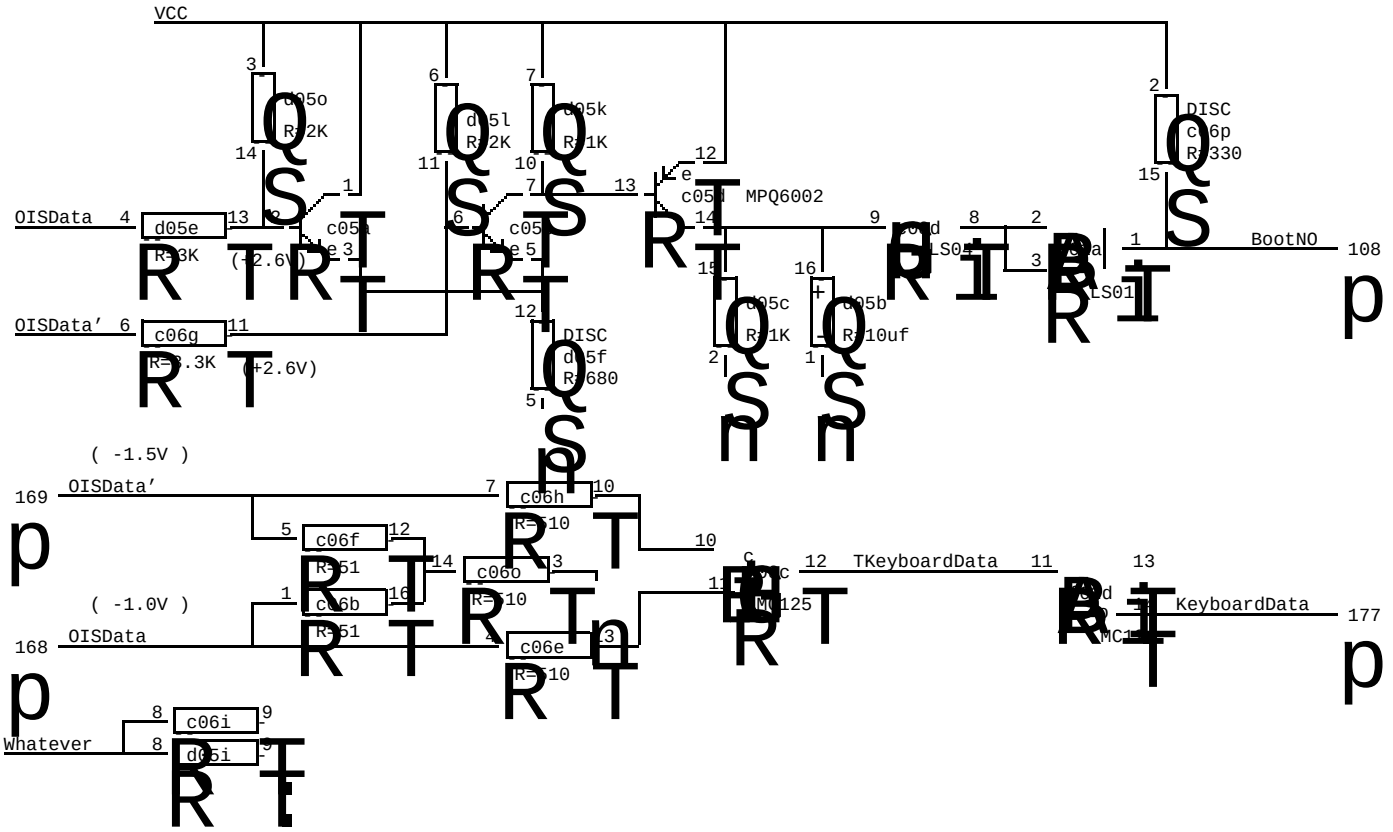


XEROX CSL	Project Dorado	Temp Sense, Pwr On, And LED Ckts	File BaseBd14.sil	Designer McCreight	Rev Am	Date 7/27/79	Page 14

Expansion EPROMS

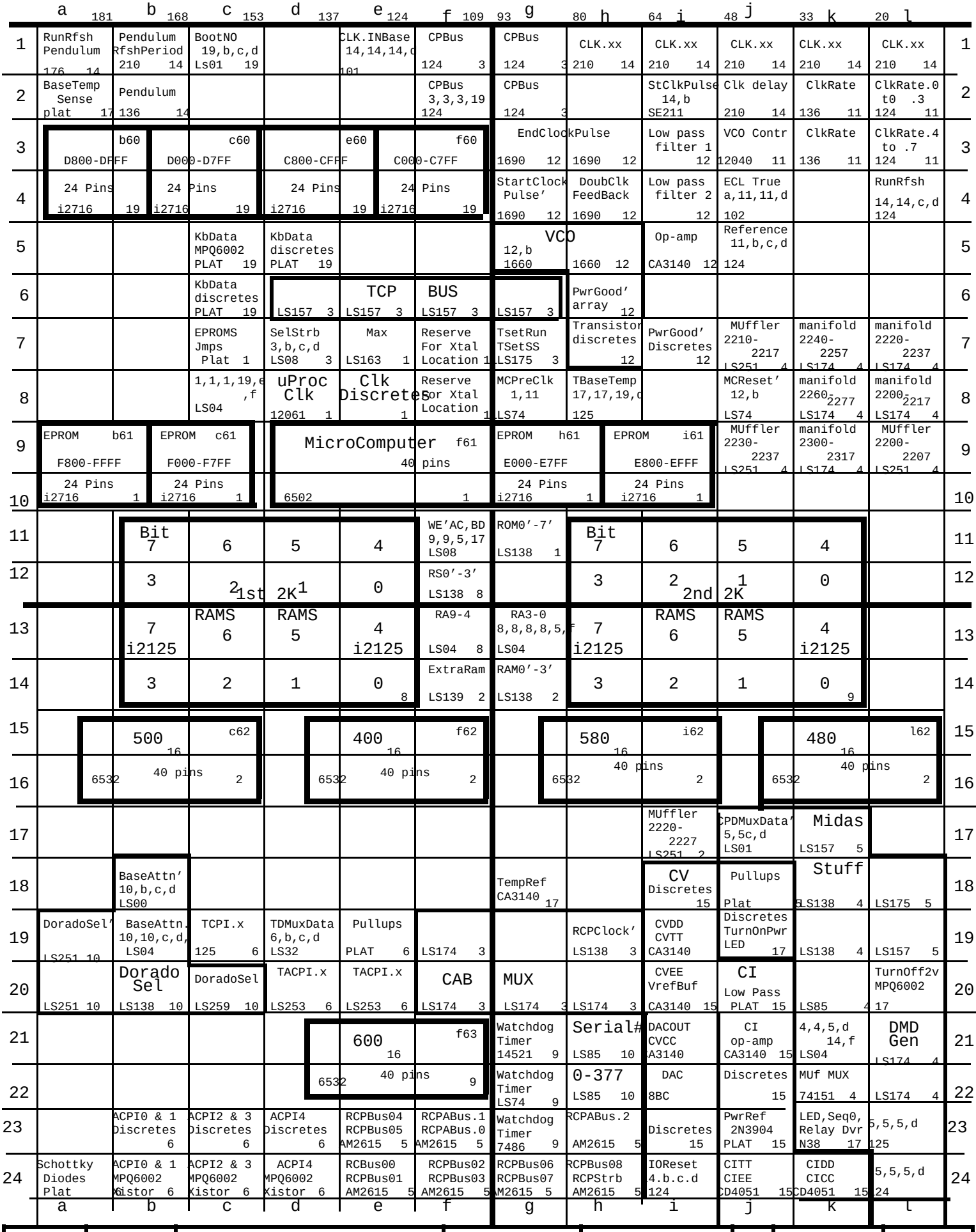


Differential Receivers for 7 wire Interface

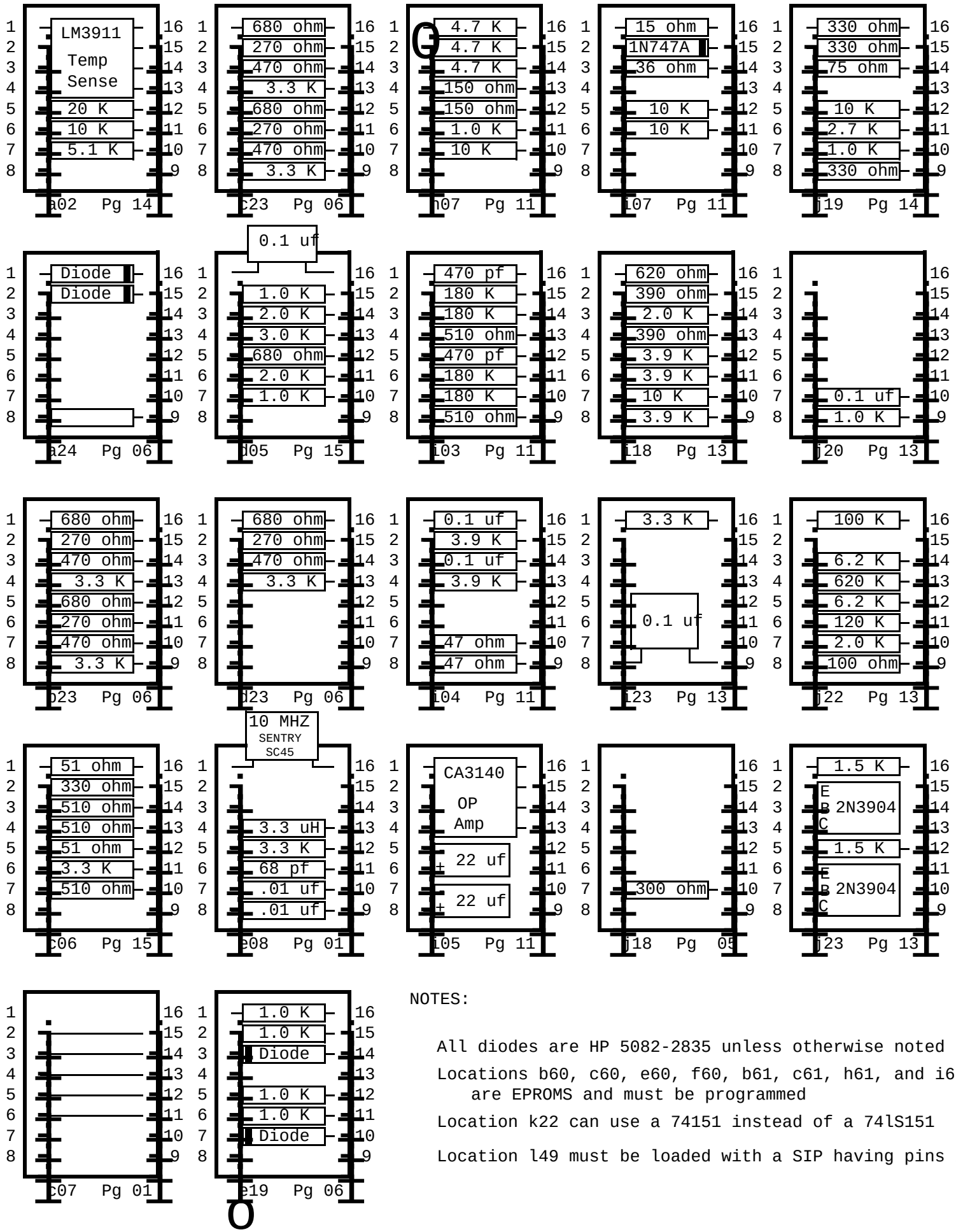


For c05

XEROX CSL	Project Dorado	Additional EPROMS & Dif Rec'er for 7 Wire Inf.	File BaseBd15.sil	Designer McCreight	Rev Am	Date 7/27/79	Page 15



XEROX CSL	Project Dorado	Reference Board Layout	File BaseBd16.sil	Designer McCreight	Rev Am	Date 4/29/81	Page 16



NOTES:

All diodes are HP 5082-2835 unless otherwise noted

Locations b60, c60, e60, f60, b61, c61, h61, and i61 a
are EPROMS and must be programmed

Location k22 can use a 74151 instead of a 74lS151

Location l49 must be loaded with a SIP having pins 2,4

XEROX CSL	Project	Reference	File	Drawings	Rev	Date	Page
	Dorado	Stuffing Information	BaseBd17.si	Sosinski	Am	7/31/79	17