

# The Dorado: A High-Performance Personal Computer

## Three Papers

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### ABSTRACT

This report reproduces three papers on the Dorado personal computer. Each has been, or will be, published in a journal or proceedings.

A Processor for a High-Performance Personal Computer, by Butler W. Lampson and Kenneth A. Pier. Appeared in Proc. 7th Symposium on Computer Architecture, SigArch/IEEE, La Baule, May 1980, 146-160.

An Instruction Fetch Unit for a High-Performance Personal Computer, by Butler W. Lampson, Gene A. McDaniel, and Severo M. Ornstein. Submitted for publication.

The Memory System of a High-Performance Personal Computer, by Douglas W. Clark, Butler W. Lampson, and Kenneth A. Pier. A revised version will appear in IEEE Transactions on Computers.

The first paper describes the Dorado's micro-programmed processor, and also gives an overview of its history and physical construction. The second discusses the instruction unit, which prepares program instructions for execution, and the third deals with the cache map and main storage of the Dorado's memory system.

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