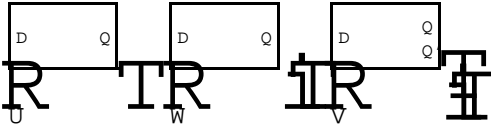
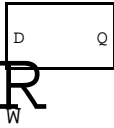
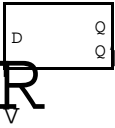
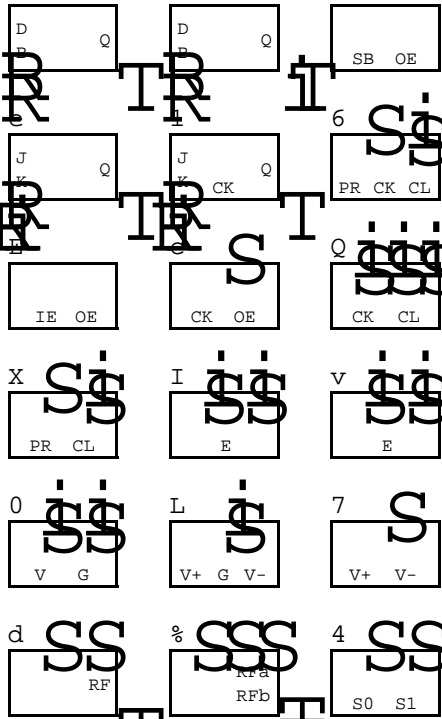









a 
 b 
 p 

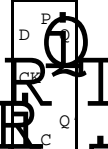

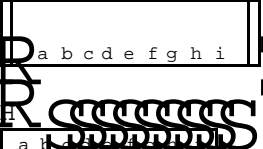


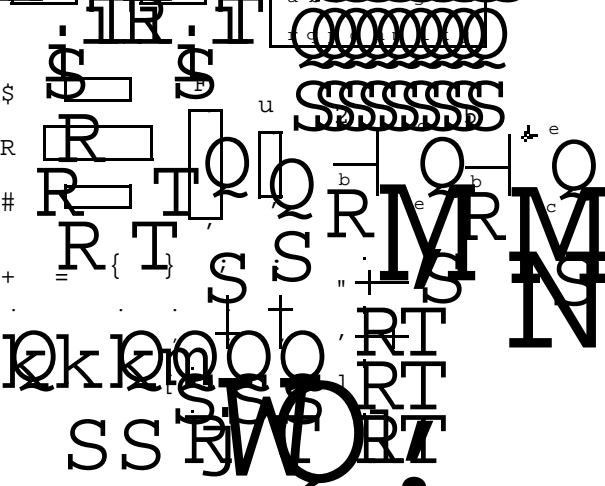
SS

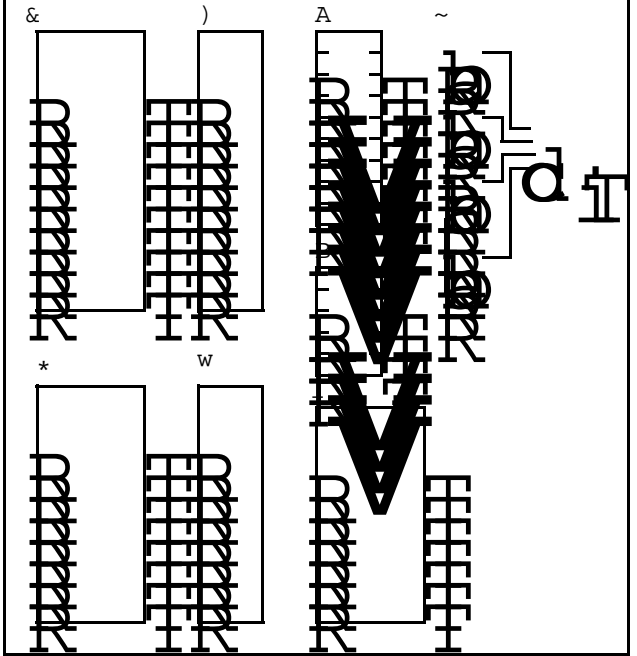
f 
 g 
 n 
 o 
 p 
 q 



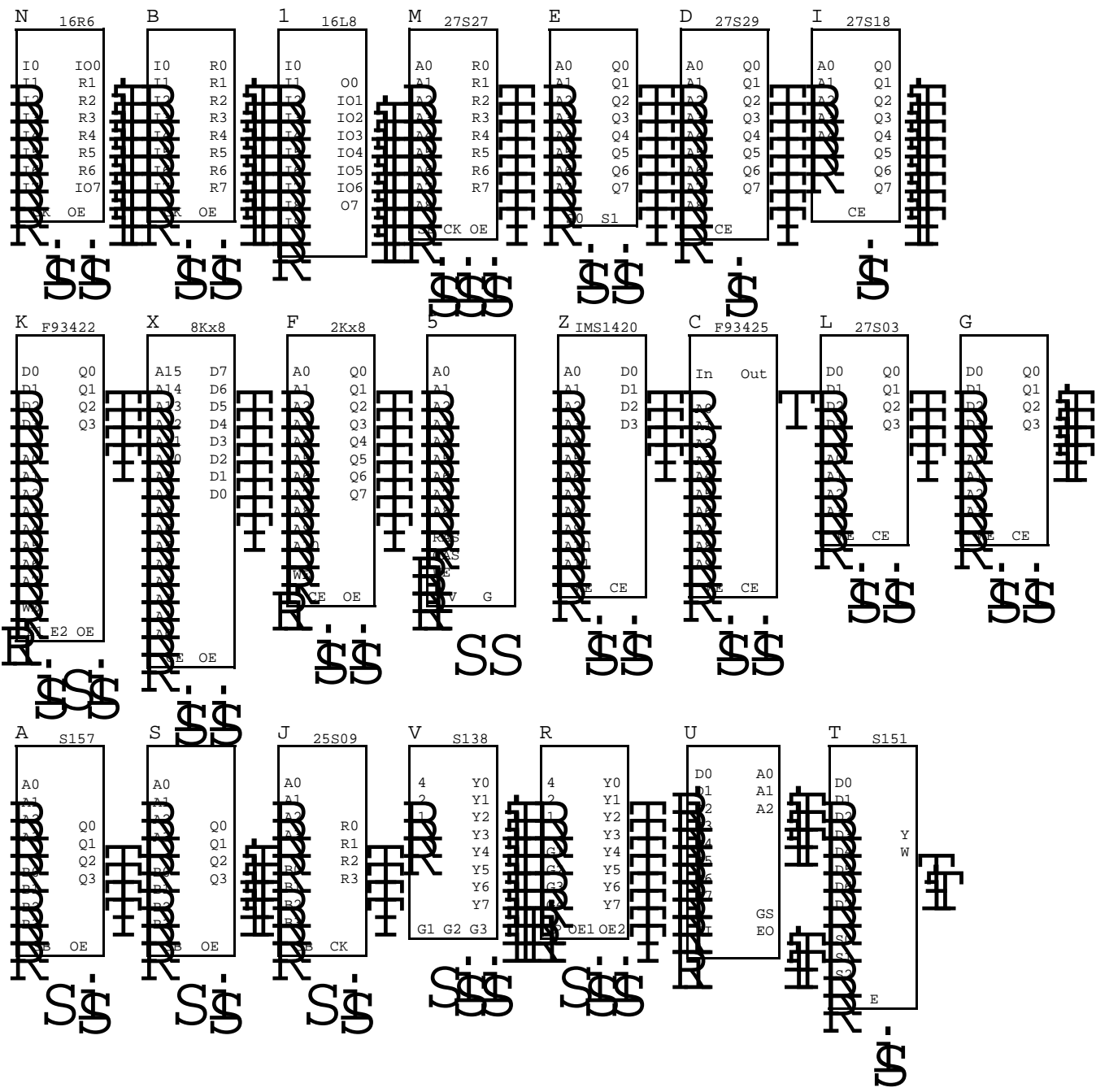
WARNING! You must set the pin numbers on these macros by hand.

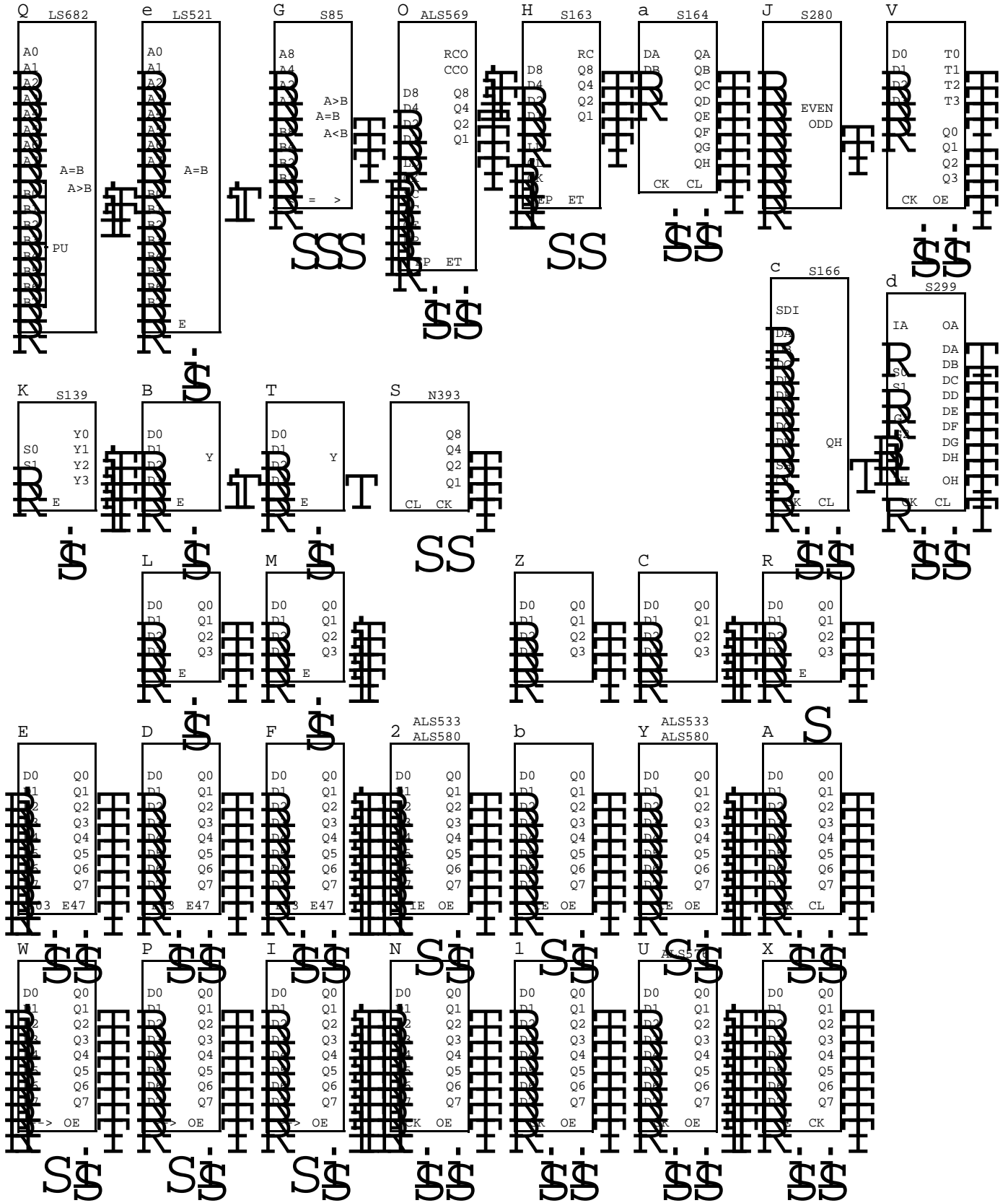
z 
 9 
 G 





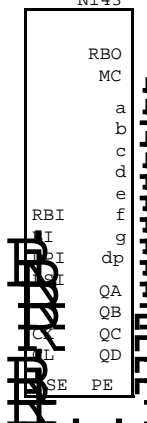
Available M N 3 ? / > . < , ^ _ \ |





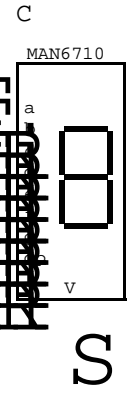
B N143

RBO
MC
a
b
c
d
e
f
g
dp
QA
QB
QC
QD
PE



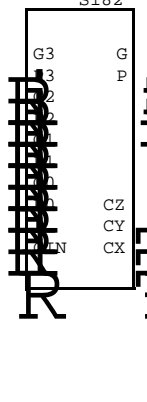
C MAN6710

a



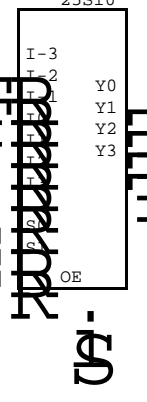
F S182

G3 G
P
CZ
CY
CX



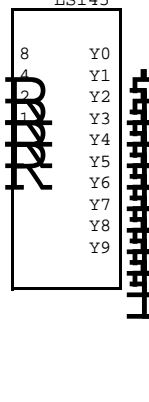
H 25S10

I-3
I-2
Y0
Y1
Y2
Y3
OE



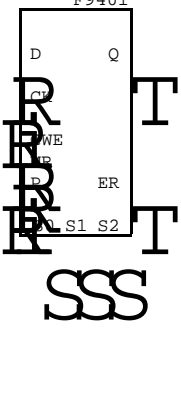
A LS145

8 Y0
Y1
Y2
Y3
Y4
Y5
Y6
Y7
Y8
Y9



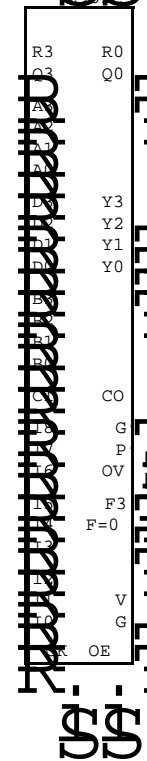
D F9401

D Q
WE
ER
S1 S2



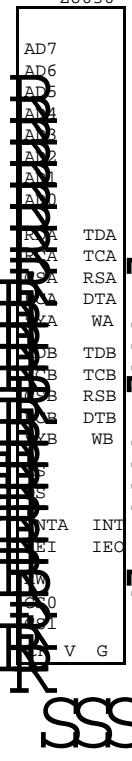
E 299

R3 R0
Q3 Q0
Y3
Y2
Y1
Y0
CO
G
P
OV
F3
F=0
V
G
OE



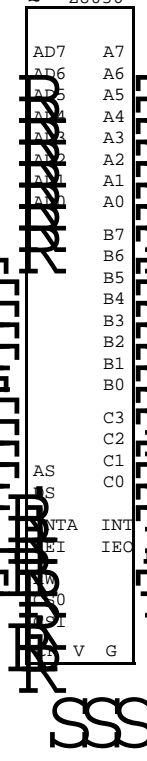
P z8030

AD7
AD6
AD5
AD4
AD3
AD2
AD1
AD0
TDA
TCA
RSA
DTA
WA
TDB
TCB
RSB
DTB
WB
INTA
INT
IEC
V
G



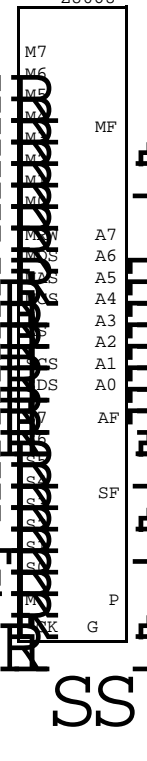
Q z8036

AD7 A7
AD6 A6
AD5 A5
AD4 A4
AD3 A3
AD2 A2
AD1 A1
AD0 A0
B7
B6
B5
B4
B3
B2
B1
B0
C3
C2
C1
C0
AS
S
INTA
INT
IEC
V
G



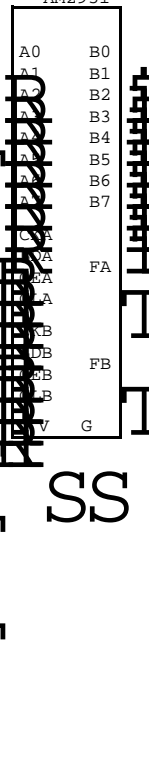
W z8068

M7
M6
M5
M4
M3
M2
M1
M0
MF
A7
A6
A5
A4
A3
A2
A1
A0
AF
SF
P
G



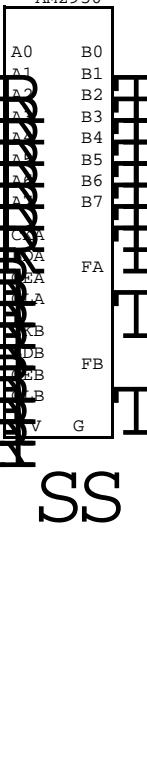
X AM2951

A0 B0
A1 B1
A2 B2
A3 B3
A4 B4
A5 B5
A6 B6
A7 B7
CA
CB
CC
CD
CE
CF
CG
CH
CI
CJ
CK
CL
FA
FB
G



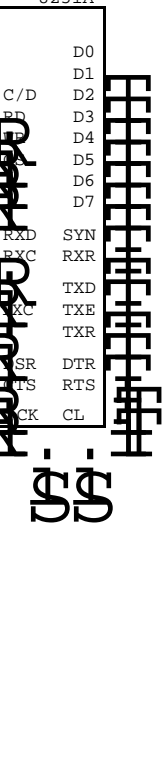
Y AM2950

A0 B0
A1 B1
A2 B2
A3 B3
A4 B4
A5 B5
A6 B6
A7 B7
CA
CB
CC
CD
CE
CF
CG
CH
CI
CJ
CK
CL
FA
FB
G



Z 8251A

D0
D1
D2
D3
D4
D5
D6
D7
C/D
RXD
RXC
TXD
TXE
TXR
DTR
RTS
RTS
CL



G

V 15V
V 15V
G
G

