

Debugging

- o Rosemary Functional Simulator

 - Diagnostic microcode checkout
 - Correspondence with layout

- o Dicentra MSI Version

 - Microcode checkout
 - IFU checkout
 - Existing Mesa debugging environment (Tajo Burdock)
 - Device Heads checkout

- o Test Chip

 - Clock Generator
 - AP Interface State Machine
 - U register access
 - ALU speed

- o On-Chip features

 - AP States
 - F/X Bus
 - Single-Step