

Cholla: A Computer Assisted Silicon Processing Facility

Overview

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Abstract: Cholla, a system providing computer assistance to the silicon wafer processing facility at the Integrated Circuit Laboratory of the Xerox Palo Alto Research Center, is described. Brief descriptions of the environment in which the system will be used and of the hardware and software components of the system are given. The goals of the Integrated Circuit Laboratory supported by Cholla are twofold: 1) to provide a fast turnaround silicon wafer fabrication facility supporting designers of VLSI components, and 2) to support VLSI process research. Cholla provides this support, allowing variability in individual wafer processing, through its management of information associated with VLSI fabrication, precise control of silicon processing equipment, and capture of data associated with such processing.

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