

Date: February 13, 1983 1:34 PM
 From: Larry Clark, Lynne McCreight, and Frank Vest
 Subject: Dorado Board Revision Status
 To: DoradoCore^.pa
 Reply-To: Clark

Filed on: [Indigo]<DoradoDocs>RevStatus.press
 [Indigo]<DoradoLogic>RevStatus.bravo

This is a list of the revision levels for the Dorado logic drawings.
 Boards should be marked to reflect the current reworked board revision level.
 The documentation will be updated soon. Corrections and updates requested.

BOARD	REV	CHANGES
BaseBoard	swAk	Unknown
	swAl	Watchdog timer fix
	swAm	Fix timer error; Current
	pc Bb	Unknown
	pc Bc	Watchdog timer
	pc Bd	Fix timer error
	pc Be	Unknown
	pc Bf	Unknown
	pc Bg	Resistors changed from 5% to 1%; Current
		Notes: 1. Last pc logic drawing rev is Be. 2. Pull-up resistors have been added to the 3140 Op Amps on the board but the documentation has not been changed.
ContB	swCd	Current
	mwCd	Current
	apcDa	Current
ContA	swCd	Current
	mwCd	Current
	apcDa	Current
Procl	swCi	Null
	mwCf	Unknown
	mwCg	Null
	mwCh	Fix to "Hold" signal. Current
	mwCi	Terminator resistors added to selected mw boards Rev Ci withdrawn for production boards
	apcDa	mw to apc
	apcDb	Correct artwork errors; Current
		Note: Last apc logic drawing rev is Da.
Proch	mwCd	Unknown
	mwCe	Null
	mwCe	Current
	apcDa	mw to apc
	apcDb	Correct artwork errors; Current
		Note: Last apc logic drawing rev is Da.
IFU	swCh	Current
	mwCe	Unknown

	mwCf mwCg mwCh apcDa apcDb	Multiwire layout change Fix interrupts after XFER Current mw to apc Correct artwork errors; Current
		Note: Last apc logic drawing rev is Da.
MemC	swBe mwBd mwBe apcBe apcDa	Null; Current Unknown Null; Current mw to apc; Current (Used for Resource boards only.) Null; Current
		Note: Last apc logic drawing rev is Be.
MemX	Cc swCg swCh mwCg mwCh apcDa apcDb	Old rev; Not reworkable Unknown Current Unknown Current mw to apc Correct artwork errors; Current
		Notes: 1. Last apc logic drawing rev is Da. 2. Socket with .01 uF caps added to 64K map chip locations but the documentation has not been changed. 3. On MemX23.sil the 22 uF cap locations at b13 and c11 need correction but the documentation has not been changed.
MemD	swBe swCa mwBe mwCa apcDa	Current; 1k cache chips Current; 4k cache chips Current; 1k cache chips Current; 4k cache chips mw to apc; 1k cache chips; Current
MSA	swBg pcPa	Current Current: also known as "PCMSA"
		Note: Chip j2 pinout is wrong and clock at c1.9 is missing on logic drawings.
DskEth	swBd swCe swCf mwCe mwCf apcDa apcDb	2 boards in service; cannot be reworked Null Current Null Current mw to apc Power off disk protect; Current
		Notes: 1. Plats added at a18, f03, and f06 but the documentation has not been changed.
DispY	swCh swCi swCj swCk	Fix DispM color problems Unknown LF Display mod. Trial 1000 line display enhancement

	mwCh mwCi mwCj mwCk apcDa apcDb	Fix DispM color problems Unknown LF Display mod 1000 line display enhancement; not used mw to apc; created from Cj Correct artwork errors; Current
		Notes: 1. Chip in a02 not required but the documentation has not been changed.
DispM	swCg swCh mwCg mwCh apcDa	Unknown add prog PhaseRef and NTSC crystal option; 1 exists Current; not upgraded to Ch add prog PhaseRef and NTSC crystal option; 1 exists mw to apc; Current
		Notes: 1. apc does not yet exist.
Special Board:		
Music	swAc	Current